



# A Concurrent Calculus with Atomic Transactions

Lucia Acciai, Michele Boreale, Silvano Dal Zilio

## ► To cite this version:

Lucia Acciai, Michele Boreale, Silvano Dal Zilio. A Concurrent Calculus with Atomic Transactions. 2006. hal-00109264

**HAL Id: hal-00109264**

**<https://hal.science/hal-00109264>**

Preprint submitted on 24 Oct 2006

**HAL** is a multi-disciplinary open access archive for the deposit and dissemination of scientific research documents, whether they are published or not. The documents may come from teaching and research institutions in France or abroad, or from public or private research centers.

L'archive ouverte pluridisciplinaire **HAL**, est destinée au dépôt et à la diffusion de documents scientifiques de niveau recherche, publiés ou non, émanant des établissements d'enseignement et de recherche français ou étrangers, des laboratoires publics ou privés.

# A Concurrent Calculus with Atomic Transactions

Lucia Acciai<sup>1</sup>, Michele Boreale<sup>2</sup>, and Silvano Dal Zilio<sup>1</sup>

<sup>1</sup> Laboratoire d'Informatique Fondamentale de Marseille (LIF),  
CNRS and Université de Provence, France

<sup>2</sup> Dipartimento di Sistemi e Informatica, Università di Firenze, Italy

**Abstract** The *Software Transactional Memory* (STM) model is an original approach for controlling concurrent accesses to resources without the need for explicit lock-based synchronization mechanisms. A key feature of STM is to provide a way to group sequences of read and write actions inside *atomic blocks*, similar to database transactions, whose whole effect should occur atomically.

In this paper, we investigate STM from a process algebra perspective and define an extension of asynchronous CCS with atomic blocks of actions. Our goal is not only to set a formal ground for reasoning on STM implementations but also to understand how this model fits with other concurrency control mechanisms. We also view this calculus as a test bed for extending process calculi with atomic transactions. This is an interesting direction for investigation since, for the most part, actual works that mix transactions with process calculi consider compensating transactions, a model that lacks all the well-known ACID properties.

We show that the addition of atomic transactions results in a very expressive calculus, enough to easily encode other concurrent primitives such as guarded choice and multiset-synchronization (à la join-calculus). The correctness of our encodings is proved using a suitable notion of bisimulation equivalence. The equivalence is then applied to prove interesting “laws of transactions” and to obtain a simple normal form for transactions.

## 1 Introduction

The craft of programming concurrent applications is about mastering the strains between two key factors: getting hold of results as quickly as possible, while ensuring that only correct results (and behaviors) are observed. To this end, it is vital to avoid unwarranted access to shared resources. The *Software Transactional Memory* (STM) model is an original approach for controlling concurrent accesses to resources without using explicit lock-based synchronization mechanisms. Similarly to database transactions, the STM approach provides a way to group sequences of read and write actions inside *atomic blocks* whose whole effect should occur atomically. The STM model has several advantages. Most notably, it dispenses the programmer with the need to explicitly manipulate locks, a task widely recognized as difficult and error-prone. Moreover, atomic transactions provide a clean conceptual basis for concurrency control, which should ease the verification of concurrent programs. Finally, the model is effective: there exist several STM implementations for designing software for multiprocessor systems; these applications exhibit good performances in practice (compared to equivalent, hand-crafted, code using locks).

We investigate the STM model from a process algebra perspective and define an extension of asynchronous CCS [22] with atomic blocks of actions. We call this calculus ATCCS. The choice of a dialect of CCS is motivated by an attention to economy: to focus on STM primitives, we study a calculus as simple as possible and dispense with orthogonal issues such as values,

mobility of names or processes, *etc.* We believe that our work could be easily transferred to a richer setting. Our goal is not only to set a formal ground for reasoning on STM implementations but also to understand how this model fits with other concurrency control mechanisms. We also view this calculus as a test bed for extending process calculi with atomic transactions. This is an interesting direction for investigation since, for the most part, works that mix transactions with process calculi consider *compensating transactions*, see e.g. [2,4,6,8,9,11,12,13,21].

The idea of providing hardware support for software transactions originated from works by Herlihy and Moss [20] and was later extended by Shavit and Touitou [25] to software-only transactional memory. Transactions are used to protect the execution of an atomic block. Intuitively, each thread that enters a transaction takes a snapshot of the shared memory (the global state). The evaluation is optimistic and all actions are performed on a copy of the memory (the local state). When the transaction ends, the snapshot is compared with the current state of the memory. There are two possible outcomes: if the check indicates that concurrent writes have occurred, the transaction aborts and is rescheduled; otherwise, the transaction is committed and its effects are propagated instantaneously. Very recently, Harris et al. [19] have proposed a (combinator style) language of transactions that enables arbitrary atomic operations to be composed into larger *atomic expressions*. We base the syntax of ATCCS on the operators defined in [19].

The main contributions of this work are: (1) the definition of a process calculus with atomic transactions; and (2) the definition of an asynchronous bisimulation equivalence  $\approx_a$  that allows compositional reasoning on transactions. We also have a number of more specific technical results. We show that ATCCS is expressive enough to easily encode interesting concurrent primitives, such as (preemptive versions of) guarded choice and multiset-synchronization, and the leader election problem (Section 3). Next, we define an equivalence between atomic expressions  $\simeq$  and prove that  $\approx_a$  and  $\simeq$  are congruences (Section 4). These equivalences are used to prove the correctness of our encodings, to prove interesting “behavioral laws of transactions” and to define a simple normal form for transactions. We also show that transactions (modulo  $\simeq$ ) have an algebraic structure close to that of a bound semilattice, an observation that could help improve the design of the transaction language. Finally, we propose a may-testing equivalence for ATCCS, give an equivalent characterization using a trace-based semantics and show that may testing equivalence is unable to notice the presence of transactions (Section 5). Section 6 concludes with an overview on future and related works. The proofs of the main results are reported in the appendices.

## 2 The calculus

We define the syntax and operational semantics of ATCCS, which is essentially a cut down version of asynchronous CCS, without choice and relabeling operators, equipped with atomic blocks and constructs for composing (transactional) sequences of actions.

**Syntax of Processes and Atomic Expressions.** The syntax of ATCCS, given in Table 1, is divided into syntactical categories that define a stratification of terms. The definition of the calculus depends on a set of names, ranged over by  $a, b, \dots$ . As in CCS, names model communication channels used in process synchronization, but they also occur as objects of read and write actions in atomic transactions.

*Atomic expressions*, ranged over by  $M, N, \dots$ , are used to define sequences of actions whose effect should happen atomically. Actions  $\text{rd } a$  and  $\text{wt } a$  represent attempts to input and output to the channel  $a$ . Instead of using snapshots of the state for managing transaction,

Actions	$\alpha, \beta ::= \text{rd } a$	(tentative) read access to $a$
	$  \text{wt } a$	(tentative) write access to $a$
(Atomic) Expressions	$M, N ::= \text{end}$	termination
	$  \text{retry}$	abort and retry the current atomic block
	$  \alpha.M$	action prefix
	$  M \text{ orElse } N$	alternative
Ongoing expressions	$A, B ::= (M)_{\sigma, \delta}$	execution of $M$ with state $\sigma$ and log $\delta$
	$  A \text{ orElse } B$	ongoing alternative
Processes	$P, Q ::= \mathbf{0}$	nil
	$  \bar{a}$	(asynchronous) output
	$  a.P$	input
	$  *a.P$	replicated input
	$  P \mid Q$	parallel composition
	$  P \setminus^n a$	hiding
	$  \text{atom}(M)$	atomic block
	$  \{A\}_M$	ongoing atomic block

**Table 1.** Syntax of ATCCS: Processes and Atomic Expressions.

we use a log-based approach. During the evaluation of an atomic block, actions are recorded in a private log  $\delta$  (a sequence  $\alpha_1 \dots \alpha_n$ ) and have no effects outside the scope of the transaction until it is committed. The action `retry` aborts an atomic expression unconditionally and starts its execution afresh, with an empty log  $\varepsilon$ . The termination action `end` signals that an expression is finished and should be committed. If the transaction can be committed, all actions in the log are performed at the same time and the transaction is closed, otherwise the transaction aborts. Finally, transactions can be composed using the operator `orElse`, which implements (preemptive) alternatives between expressions.  $M \text{ orElse } N$  behaves as expression  $N$  if  $M$  aborts and has the behavior of  $M$  otherwise.

*Processes*, ranged over by  $P, Q, R, \dots$ , model concurrent systems of communicating agents. We have the usual operators of CCS: the empty process,  $\mathbf{0}$ , the parallel composition  $P \mid Q$ , and the input prefix  $a.P$ . There are some differences though. The calculus is asynchronous, meaning that a process cannot block on output actions. Also, we use *replicated input*  $*a.P$  instead of recursion (this does not change the expressiveness of the calculus) and we lack the choice and relabeling operators of CCS. Finally, the main addition is the presence of the operator  $\text{atom}(M)$ , which models a transaction that safeguards the expression  $M$ . The process  $\{A\}_M$  represents the ongoing evaluation of an atomic block  $M$ : the subscript is used to keep the initial code of the transaction, in case it is aborted and executed afresh, while  $A$  holds the remaining actions that should be performed.

An *ongoing atomic block*,  $A, B, \dots$ , is essentially an atomic expression enriched with an *evaluation state*  $\sigma$  and a *log*  $\delta$  of the currently recorded actions. A state  $\sigma$  is a multiset of names that represents the output actions visible to the transaction when it was initiated. (This notion

of state bears some resemblance with tuples space in coordination calculi, such as Linda [10].) When a transaction ends, the state  $\sigma$  recorded in the block  $(M)_{\sigma;\delta}$  (the state at the initiation of the transaction) can be compared with the current state (the state when the transaction ends) to check if other processes have concurrently made changes to the global state, in which case the transaction should be aborted.

**Notation.** In the following, we write  $\sigma \uplus \{a\}$  for the multiset  $\sigma$  enriched with the name  $a$  and  $\sigma \setminus \sigma'$  for the multiset obtained from  $\sigma$  by removing elements found in  $\sigma'$ , that is the smallest multiset  $\sigma''$  such that  $\sigma \subseteq \sigma' \uplus \sigma''$ . The symbol  $\emptyset$  stands for the empty multiset while  $\{a^n\}$  is the multiset composed of exactly  $n$  copies of  $a$ , where  $\{a^0\} = \emptyset$ .

Given a log  $\delta$ , we use the notation  $\text{WT}(\delta)$  for the multiset of names which appear as objects of a write action in  $\delta$ . Similarly, we use the notation  $\text{RD}(\delta)$  for the multiset of names that are objects of read actions. The functions  $\text{WT}$  and  $\text{RD}$  may be inductively defined as follows:  $\text{WT}(\text{wt } a . \delta) = \text{WT}(\delta) \uplus \{a\}$ ;  $\text{RD}(\text{rd } a . \delta) = \text{RD}(\delta) \uplus \{a\}$ ;  $\text{WT}(\text{rd } a . \delta) = \text{RD}(\text{wt } a . \delta) = \text{WT}(\delta)$ ; and  $\text{WT}(\varepsilon) = \text{RD}(\varepsilon) = \varepsilon$ .

**Example: Composing Synchronization.** Before we describe the meaning of processes, we try to convey the semantics of ATCCS (and the usefulness of the atomic block operator) using a simple example. We take the example of a concurrent system with two memory cells,  $M_1$  and  $M_2$ , used to store integers. We consider here a straightforward extension of the calculus with “value-passing<sup>1</sup>.” In this setting, we can model a cell with value  $v$  by an output  $\overline{m_i}!v$  and model an update by a process of the form  $m_i?x.(\overline{m_i}!v' \mid \dots)$ . With this encoding, the channel name  $m_i$  acts as a lock protecting the shared resource  $M_i$ .

Assume now that the values of the cells should be synchronized to preserve a global invariant on the system. For instance, we model a flying aircraft, each cell store the pitch of an aileron and we need to ensure that the aileron stay aligned (that the values of the cells are equal). A process testing the validity of the invariant is for example  $P_1$  below (we suppose that a message on the reserved channel *err* triggers an alarm). There are multiple design choices for resetting the value of both cells to 0, e.g.  $P_2$  and  $P_3$ .

$$\begin{aligned} P_1 &\triangleq m_1?x.m_2?y.\text{if } x \neq y \text{ then } \overline{err}! \\ P_2 &\triangleq m_2?x.m_1?y.(\overline{m_1}!0 \mid \overline{m_2}!0) \quad P_3 \triangleq m_1?x.(\overline{m_1}!0 \mid m_2?y.\overline{m_2}!0) \end{aligned}$$

Each choice exemplify a problem with lock-based programming. The composition of  $P_1$  with  $P_2$  leads to a race condition where  $P_1$  acquire the lock on  $M_1$ ,  $P_2$  on  $M_2$  and each process gets stuck. The composition of  $P_1$  and  $P_3$  may break the invariant (the value of  $M_1$  is updated too quickly). A solution in the first case is to strengthen the invariant and enforce an order for acquiring locks, but this solution is not viable in general and opens the door to *priority inversion* problems. Another solution is to use an additional (master) lock to protect both cells, but this approach obfuscate the code and significantly decreases the concurrency of the system.

Overall, this simple example shows that synchronization constraints do not compose well when using locks. This situation is consistently observed (and bears a resemblance to the inheritance anomaly problem found in concurrent object-oriented languages). The approach advocated in this paper is to use atomic transactions. In our example, the problem is solved by simply wrapping the two operations in a transaction, like in the following process, which en-

<sup>1</sup> Keeping to attention to economy in the definition of ATCCS, we choose not to consider values in the formal syntax, but our results could be easily extended to take them into account.

(OUT) $\bar{a}; \sigma \rightarrow \mathbf{0}; \sigma \uplus \{a\}$	(REP) $*a.P; \sigma \uplus \{a\} \rightarrow P \mid *a.P; \sigma$
(IN) $a.P; \sigma \uplus \{a\} \rightarrow P; \sigma$	(COM) $\frac{P; \sigma \rightarrow P'; \sigma \uplus \{a\} \quad Q; \sigma \uplus \{a\} \rightarrow Q'; \sigma}{P \mid Q \rightarrow P' \mid Q'}$
(PARL) $\frac{P; \sigma \rightarrow P'; \sigma'}{P \mid Q; \sigma \rightarrow P' \mid Q; \sigma'}$	(HID) $\frac{P; \sigma \uplus \{a^n\} \rightarrow P'; \sigma' \uplus \{a^m\} \quad a \notin \sigma, \sigma'}{P \setminus^n a; \sigma \rightarrow P' \setminus^m a; \sigma'}$
(PARR) $\frac{Q; \sigma \rightarrow Q'; \sigma'}{P \mid Q; \sigma \rightarrow P \mid Q'; \sigma'}$	(ATST) $\text{atom}(M); \sigma \rightarrow \llbracket (M)_{\sigma; \varepsilon} \rrbracket_M; \sigma$
(ATPASS) $\frac{A \rightarrow A'}{\llbracket A \rrbracket_M; \sigma \rightarrow \llbracket A' \rrbracket_M; \sigma}$	(ATRE) $\llbracket (\text{retry})_{\sigma'; \delta} \rrbracket_M; \sigma \rightarrow \text{atom}(M); \sigma$
(ATFAIL) $\frac{\text{RD}(\delta) \not\subseteq \sigma}{\llbracket (\text{end})_{\sigma'; \delta} \rrbracket_M; \sigma \rightarrow \text{atom}(M); \sigma}$	
(ATOK) $\frac{\text{RD}(\delta) \subseteq \sigma \quad \sigma = \sigma'' \uplus \text{RD}(\delta) \quad \text{WT}(\delta) = \{a_1, \dots, a_n\}}{\llbracket (\text{end})_{\sigma'; \delta} \rrbracket_M; \sigma \rightarrow \bar{a}_1 \mid \dots \mid \bar{a}_n; \sigma''}$	

**Table2.** Operational Semantics: Processes.

sures that all cell updates are effected atomically.

$$\text{atom}(\text{rd}(m_2?y).\text{wt}(m_2!0).\text{rd}(m_1?x).\text{wt}(m_1!0))$$

More examples may be found on the paper on composable memory transactions [19], which makes a compelling case that “even correctly-implemented concurrency abstractions cannot be composed together to form larger abstractions.”

**Operational Semantics.** Like for the syntax, the semantics of ATCCS is stratified in two levels: there is one reduction relation for processes and a second for atomic expressions. With a slight abuse of notation, we use the same symbol ( $\rightarrow$ ) for both relations.

*Reduction for Processes.* Table 2 gives the semantics of processes. A reduction is of the form  $P; \sigma \rightarrow P'; \sigma'$  where  $\sigma$  is the state of  $P$ . The state  $\sigma$  records the names of all output actions visible to  $P$  when reduction happens. It grows when an output is reduced, (OUT), and shrinks in the case of inputs, (IN) and (REP). A parallel composition evolves if one of the component evolves or if both can synchronize, rules (PARL), (PARR) and (COM). In a hiding  $P \setminus^n a$ , the annotation  $n$  is an integer denoting the number of outputs on  $a$  which are visible to  $P$ . Intuitively, in a “configuration”  $P \setminus^n a; \sigma$ , the outputs visible to  $P$  are those in  $\sigma \uplus \{a^n\}$ . This extra annotation is necessary because the scope of  $a$  is restricted to  $P$ , hence it is not possible to have outputs on  $a$  in the global state. Rule (HID) allows synchronization on the name  $a$  to happen inside a hiding. For instance, we have  $(P \mid \bar{a}) \setminus^n a; \sigma \rightarrow P \setminus^{n+1} a; \sigma$ .

The remaining reduction rules govern the evolution of atomic transactions. Like in the case of (COM), all those rules, but (ATOK), leave the global state unchanged. Rule (ATST) deals with the initiation of an atomic block  $\text{atom}(M)$ : an ongoing block  $\llbracket (M)_{\sigma; \varepsilon} \rrbracket_M$  is created which holds the current evaluation state  $\sigma$  and an empty log  $\varepsilon$ . An atomic block  $\llbracket A \rrbracket_M$  reduces when its expression  $A$  reduces, rule (ATPASS). (The reduction relation for ongoing expression is defined by the rules in Table 3.) Rules (ATRE), (ATFAIL) and (ATOK) deal with the completion of a transaction. After a finite number of transitions, the evaluation of an ongoing expression

(ARDOK) $\frac{RD(\delta) \uplus \{a\} \subseteq \sigma}{(rd\ a.M)_{\sigma;\delta} \rightarrow (M)_{\sigma;\delta.rd\ a}}$	(ARDF) $\frac{RD(\delta) \uplus \{a\} \not\subseteq \sigma}{(rd\ a.M)_{\sigma;\delta} \rightarrow (retry)_{\sigma;\delta}}$
(AWR) $(wt\ a.M)_{\sigma;\delta} \rightarrow (M)_{\sigma;\delta.wt\ a}$	
(AOI) $(M_1\ orElse\ M_2)_{\sigma;\delta} \rightarrow (M_1)_{\sigma;\delta}\ orElse\ (M_2)_{\sigma;\delta}$	
(AOF) $(retry)_{\sigma;\delta}\ orElse\ B \rightarrow B$	(AOE) $(end)_{\sigma;\delta}\ orElse\ B \rightarrow (end)_{\sigma;\delta}$
(AOL) $\frac{A \rightarrow A'}{A\ orElse\ B \rightarrow A'\ orElse\ B}$	(AOR) $\frac{B \rightarrow B'}{A\ orElse\ B \rightarrow A\ orElse\ B'}$

**Table3.** Operational Semantics : Ongoing Atomic Expression.

will necessarily result in a fail state,  $(retry)_{\sigma;\delta}$ , or a success,  $(end)_{\sigma;\delta}$ . In the first case, rule (ATRE), the transaction is aborted and started again from scratch. In the second case, we need to check if the log is consistent with the current evaluation state. A log is consistent if the read actions of  $\delta$  can be performed on the current state. If the check fails, rule (ATFAIL), the transaction aborts. Otherwise, rule (ATOK), we commit the transaction: the names in  $RD(\delta)$  are taken from the current state and a bunch of outputs on the names in  $WT(\delta)$  are generated.

*Reduction for Ongoing Expressions.* Table 3 gives the semantics of ongoing atomic expressions. We recall that, in an expression  $(rd\ a.M)_{\sigma;\delta}$ , the subscript  $\sigma$  is the *initial state*, that is a copy of the state at the time the block has been created and  $\delta$  is the log of actions performed since the initiation of the transaction.

Rule (ARDOK) states that a read action  $rd\ a$  is recorded in the log  $\delta$  if all the read actions in  $\delta.rd\ a$  can be performed in the initial state. If it is not the case, the ongoing expression fails, rule (ARDF). This test may be interpreted as a kind of optimization: if a transaction cannot commit in the initial state then, should it commit at the end of the atomic block, it would mean that the global state has been concurrently modified during the execution of the transaction. Note that we consider the initial state  $\sigma$  and not  $\sigma \uplus WT(\delta)$ , which means that, in an atomic block, write actions are not directly visible (they cannot be consumed by a read action). This is coherent with the fact that outputs on  $WT(\delta)$  only take place after commit of the block. Rule (AWR) states that a write action always succeeds and is recorded in the current log.

The remaining rules govern the semantics of the `retry`, `end` and `orElse` constructs. These constructs are borrowed from the STM combinators used in the implementation of an STM system in Concurrent Haskell [19]. We define these operators with an equivalent semantics, with the difference that, in our case, a state is not a snapshot of the (shared) memory but a multiset of visible outputs. A composition  $M\ orElse\ N$  corresponds to the interleaving of the behaviors of  $M$  and  $N$ , which are independently evaluated with respect to the same evaluation state (but have distinct logs). The `orElse` operator is preemptive: the ongoing block  $M\ orElse\ N$  ends if and only  $M$  ends or  $M$  aborts and  $N$  ends.

### 3 Encoding Concurrency Primitives

Our first example is a simple solution to the celebrated *leader election* problem that does not yield to deadlock and ensures that, at each round, a leader is elected.

Consider a system composed by  $n$  processes and a token, named  $t$ , that is modeled by an output  $\bar{t}$ . A process becomes a leader by getting (making an input on)  $t$ . As usual, all participants run the same process (except for the value of their identity). We suppose that there is only one copy of the token in the system and that leadership of process  $i$  is communicated to the other processes by outputting on a reserved name  $win_i$ . A participant that is not a leader output on  $loose_i$ . The protocol followed by the participants is defined by the following process:

$$L_i \triangleq (\text{atom}(\text{rd } t.\text{wt } k.\text{end} \text{ orElse } \text{wt } k'.\text{end}) \mid k.(\overline{win_i} \mid \bar{t}) \mid k'.\overline{loose_i}) \setminus^0 k \setminus^0 k'$$

In this encoding, the atomic block is used to protect the concurrent accesses to  $t$ . If the process  $L_i$  commits its transaction and inputs (grabs) the token, it immediately release an output on its private channel  $k$ . The transactions of the other participants may either fail or commit while releasing an output on their private channel  $k'$ . Then, the elected process  $L_i$  may proceed with a synchronization on  $k$  that triggers the output  $\overline{win_i}$  and release the lock. The semantics of  $\text{atom}()$  ensures that only one transaction can acquire the lock and commit the atomic block, then no other process have acquired the token in the same round and we are guaranteed that there could be at most one leader.

This expressivity result is mixed blessing. Indeed, it means that any implementation of the atomic operator should be able to solve the leader election problem, which is known to be very expensive in the case of loosely-coupled systems or in presence of failures (see e.g. [24] for a discussion on the expressivity of process calculi and electoral systems). On the other hand, atomic transactions are optimistic and are compatible with the use of probabilistic approaches. Therefore it is still reasonable to expect a practical implementation of ATCCS.

In the following, we show how to encode two fundamental concurrency patterns, namely (preemptive versions of) the choice and join-pattern operators.

**Guarded choice.** We consider an operator for choice,  $\mu_1.P_1 + \dots + \mu_n.P_n$ , such that every process is prefixed by an action  $\mu_i$  that is either an output  $\bar{a}_i$  or an input  $a_i$ . The semantics of choice is characterized by the following three reduction rules (we assume that  $Q$  is also a choice):

$$\begin{aligned} \text{(C-INP)} \quad a.P + Q; \sigma \uplus \{a\} &\rightarrow P; \sigma & \text{(C-OUT)} \quad \bar{a}.P + Q; \sigma &\rightarrow P; \sigma \uplus \{a\} \\ \text{(C-PASS)} \quad \frac{a \notin \sigma \quad Q; \sigma \rightarrow Q'; \sigma'}{a.P + Q; \sigma \rightarrow Q'; \sigma'} & \end{aligned}$$

A minor difference with the behavior of the choice operator found in CCS is that our semantics gives precedence to the leftmost process (this is reminiscent of the preemptive behavior of  $\text{orElse}$ ). Another characteristic is related to the asynchronous nature of the calculus, see rule (C-OUT): since an output action can always interact with the environment, a choice  $\bar{a}.P + Q$  may react at once and release the process  $\bar{a} \mid P$ .

Like in the example of the leader election problem, we can encode a choice  $\mu_1.P_1 + \dots + \mu_n.P_n$  using an atomic block that will mediate the interaction with the actions  $\mu_1, \dots, \mu_n$ . We start by defining a straightforward encoding of input/output actions into atomic actions:  $\llbracket \bar{a} \rrbracket = \text{wt } a$  and  $\llbracket a \rrbracket = \text{rd } a$ . Then the encoding of choice is the process

$$\begin{aligned} \llbracket \mu_1.P_1 + \dots + \mu_n.P_n \rrbracket &\triangleq (\text{atom}(\llbracket \mu_1 \rrbracket. \llbracket \bar{k}_1 \rrbracket.\text{end} \text{ orElse } \dots \text{ orElse } \llbracket \mu_n \rrbracket. \llbracket \bar{k}_n \rrbracket.\text{end}) \\ &\mid k_1. \llbracket P_1 \rrbracket \mid \dots \mid k_n. \llbracket P_n \rrbracket) \setminus^0 k_1 \dots \setminus^0 k_n \end{aligned}$$



The principle of the encoding is essentially the same that in our solution to the leader election problem. Actually, using the encoding for choice, we can rewrite our solution in the following form:  $L_i \triangleq t.(\overline{win_i} \mid \bar{t}) + \overline{loose_i}.0$ . Using the rules in Table 2, it is easy to see that our encoding of choice is compatible with rule (C-INP), meaning that:

$$\begin{aligned} \llbracket a.P + Q \rrbracket; \sigma \uplus \{a\} &\rightarrow^* (\{(\text{end})_{\sigma \uplus \{a\}; \text{rd } a. \text{wt } k_1} \}_{\mathcal{M}} \mid k_1. \llbracket P \rrbracket \mid \dots) \setminus^0 k_1 \setminus \dots; \sigma \uplus \{a\} \\ &\rightarrow (\overline{k_1} \mid k_1. \llbracket P \rrbracket \mid \dots) \setminus^0 k_1 \setminus \dots; \sigma \\ &\rightarrow (\llbracket P \rrbracket \mid \dots) \setminus^0 k_1 \setminus \dots; \sigma \end{aligned}$$

where the processes in parallel with  $\llbracket P \rrbracket$  are harmless. In the next section, we define a weak bisimulation equivalence  $\approx_a$  that can be used to garbage collect harmless processes in the sense that, e.g.  $(P \mid k.Q) \setminus^0 k \approx_a P$  if  $P$  has no occurrences of  $k$ . Hence, we could prove that  $\llbracket a.P + Q \rrbracket; \sigma \uplus \{a\} \rightarrow^* \approx_a \llbracket P \rrbracket; \sigma$ , which is enough to show that our encoding is correct with respect to rule (C-INP). The same is true for rules (C-OUT) and (C-PASS).

**Join Patterns.** A multi-synchronization  $(a_1 \times \dots \times a_n).P$  may be viewed as an extension of input prefix in which communication requires a synchronization with the  $n$  outputs  $\overline{a_1}, \dots, \overline{a_n}$  at once. that is, we have the reduction:

$$(J\text{-INP}) \quad (a_1 \times \dots \times a_n).P; \sigma \uplus \{a_1, \dots, a_n\} \rightarrow P; \sigma$$

This synchronization primitive is fundamental to the definition of the Gamma calculus of Banâtre and Le Métayer and of the Join calculus of Fournet and Gonthier. It is easy to see that the encoding of a multi-synchronization (input) is a simple transaction:

$$\llbracket (a_1 \times \dots \times a_n).P \rrbracket \triangleq (\text{atom}(\llbracket a_1 \rrbracket. \dots. \llbracket a_n \rrbracket. \overline{\llbracket k \rrbracket}. \text{end}) \mid k. \llbracket P \rrbracket) \setminus^0 k \quad (\text{where } k \text{ is fresh})$$

and that we have  $\llbracket (a_1 \times \dots \times a_n).P \rrbracket; \sigma \uplus \{a_1, \dots, a_n\} \rightarrow^* (\mathbf{0} \mid \llbracket P \rrbracket) \setminus^0 k; \sigma$ , where the process  $(\mathbf{0} \mid \llbracket P \rrbracket) \setminus^0 k$  is behaviorally equivalent to  $\llbracket P \rrbracket$ , that is:

$$\llbracket (a_1 \times \dots \times a_n).P \rrbracket; \sigma \uplus \{a_1, \dots, a_n\} \rightarrow^* \approx_a \llbracket P \rrbracket; \sigma$$

Based on this encoding, we can define two interesting derived operators: a mixed version of multi-synchronization,  $(\mu_1 \times \dots \times \mu_n).P$ , that mixes input and output actions; and a replicated version, that is analogous to replicated input.

$$\begin{aligned} \llbracket (\mu_1 \times \dots \times \mu_n).P \rrbracket &\triangleq (\text{atom}(\llbracket \mu_1 \rrbracket. \dots. \llbracket \mu_n \rrbracket. \overline{\llbracket k \rrbracket}. \text{end}) \mid k. \llbracket P \rrbracket) \setminus^0 k \\ \llbracket * (\mu_1 \times \dots \times \mu_n).P \rrbracket &\triangleq (\overline{\llbracket r \rrbracket} \mid * r. \text{atom}(\llbracket \mu_1 \rrbracket. \dots. \llbracket \mu_n \rrbracket. \overline{\llbracket k \rrbracket}. \text{end}) \mid * k. \llbracket P \rrbracket) \setminus^0 r \setminus^0 k \end{aligned}$$

By looking at the possible reductions of these (derived) operators, we can define derived reduction rules. Assume  $\delta$  is the log  $\llbracket \mu_1 \rrbracket. \dots. \llbracket \mu_n \rrbracket$ , we have a simulation result comparable to the case for multi-synchronization, namely:

$$\begin{aligned} \llbracket (\mu_1 \times \dots \times \mu_n).P \rrbracket; \sigma \uplus \text{RD}(\delta) &\rightarrow^* \approx_a \llbracket P \rrbracket; \sigma \uplus \text{WT}(\delta) \\ \llbracket * (\mu_1 \times \dots \times \mu_n).P \rrbracket; \sigma \uplus \text{RD}(\delta) &\rightarrow^* \approx_a \llbracket * (\mu_1 \times \dots \times \mu_n).P \rrbracket \mid \llbracket P \rrbracket; \sigma \uplus \text{WT}(\delta) \end{aligned}$$

To obtain join-definitions, we only need to combine a sequence of replicated multi-synchronizations using the choice composition defined precedently. (We also need hiding to

close the scope of the definition.) Actually, we can encode even more flexible constructs mixing choice and join-patterns. For the sake of simplicity, we only study examples of such operations. The first example is the (linear) join-pattern  $(a \times b).P \wedge (a \times c).Q$ , that may fire  $P$  if the outputs  $\{a, b\}$  are in the global state  $\sigma$  and otherwise fire  $Q$  if  $\{a, c\}$  is in  $\sigma$  (actually, real implementations of join-calculus have a preemptive semantics for pattern synchronization). The second example is the derived operator  $(a \times b) + (b \times c \times \bar{a}).P$ , such that  $P$  is fired if outputs on  $\{a, b\}$  are available or if outputs on  $\{b, c\}$  are available (in which case an output on  $a$  is also generated). These examples can be easily interpreted using atomic transactions:

$$\begin{aligned} \llbracket (a \times b).P \wedge (a \times c).Q \rrbracket &\triangleq (\text{atom}(\llbracket a \rrbracket.\llbracket b \rrbracket.\llbracket \bar{k}_1 \rrbracket.\text{end} \text{ orElse } \\ &\quad \llbracket a \rrbracket.\llbracket c \rrbracket.\llbracket \bar{k}_2 \rrbracket.\text{end}) \mid k_1.P \mid k_2.Q) \setminus^0 k_1 \setminus^0 k_2 \\ \llbracket (a \times b + b \times c \times \bar{a}).P \rrbracket &\triangleq (\text{atom}(\llbracket a \rrbracket.\llbracket b \rrbracket.\llbracket \bar{k} \rrbracket.\text{end} \text{ orElse } \\ &\quad \llbracket b \rrbracket.\llbracket c \rrbracket.\llbracket \bar{a} \rrbracket.\llbracket \bar{k} \rrbracket.\text{end}) \mid k.P) \setminus^0 k \end{aligned}$$

In the next section we define the notion of bisimulation used for reasoning on the soundness of our encodings. We also define an equivalence relation for atomic expressions that is useful for reasoning on the behavior of atomic blocks.

## 4 Bisimulation Semantics

A first phase before obtaining a bisimulation equivalence is to define a Labeled Transition System (LTS) for ATCCS processes related to the reduction semantics.

**Labeled Semantics of ATCCS.** It is easy to derive labels from the reduction semantics given in Table 2. For instance, a reduction of the form  $P; \sigma \rightarrow P'; \sigma \uplus \{a\}$  is clearly an *output transition* and we could denote it using the transition  $P \xrightarrow{\bar{a}} P'$ , meaning that the effect of the transition is to add a message on  $a$  to the global state  $\sigma$ . We formalize the notion of label and transition. Besides output actions  $\bar{a}$ , which corresponds to an application of rule (OUT), we also need *block actions*, which are multiset  $\{a_1, \dots, a_n\}$  corresponding to the commit of an atomic block, that is to the deletion of a bunch of names from the global state in rule (ATOK). Block actions include the usual labels found in LTS for CCS and are used for labeling input and communication transitions: an input actions  $a$ , which intuitively corresponds to rules (IN) and (REP), is a shorthand for the (singleton) block action  $\{a\}$ ; the silent action  $\tau$ , which corresponds to rule (COM), is a shorthand for the empty block action  $\emptyset$ . In the following, we use the symbols  $\theta, \gamma, \dots$  to range over block actions and  $\mu, \mu', \dots$  to range over labels,  $\mu ::= \bar{a} \mid \theta \mid \tau \mid a$ .

The labeled semantics for ATCCS is the smallest relation  $P \xrightarrow{\mu} P'$  satisfying the two following clauses:

1. we have  $P \xrightarrow{\bar{a}} P'$  if there is a state  $\sigma$  such that  $P; \sigma \rightarrow P'; \sigma \uplus \{a\}$ ;
2. we have  $P \xrightarrow{\theta} P'$  if there is a state  $\sigma$  such that  $P; \sigma \uplus \theta \rightarrow P'; \sigma$ .

Note that, in the case of the (derived) action  $\tau$ , we obtain from clause 2 that  $P \xrightarrow{\tau} P'$  if there is a state  $\sigma$  such that  $P; \sigma \rightarrow P'; \sigma$ . As usual, silent actions label transitions that do not modify the environment (in our case the global state) and so are invisible to an outside observer. Unlike CCS, the calculus has more examples of silent transition than mere internal synchronization, e.g. the initiation and evolution of an atomic block, see e.g. rules (ATST) and (ATPASS). Consequently, a suitable (weak) equivalence for ATCCS should not distinguish e.g. the processes

$\text{atom}(\text{retry})$ ,  $\text{atom}(\text{end})$ ,  $(a.\bar{a})$  and  $\mathbf{0}$ . The same is true with input transitions. For instance, we expect to equate the processes  $a.\mathbf{0}$  and  $\text{atom}(\text{rd } a.\text{end})$ .

Our labeled semantics for ATCCS is not based on a set of transition rules, as it is usually the case. Nonetheless, we can recover an axiomatic presentation of the semantics using the tight correspondence between labeled transitions and reductions characterized by Proposition 1.

**Proposition 1.** *Consider two processes  $P$  and  $Q$ . The following implications are true:*

- (COM) if  $P \xrightarrow{a} P'$  and  $Q \xrightarrow{\bar{a}} Q'$  then  $P \mid Q \xrightarrow{\tau} P' \mid Q'$ ;
- (PAR) if  $P \xrightarrow{\mu} P'$  then  $P \mid Q \xrightarrow{\mu} P' \mid Q$  and  $Q \mid P \xrightarrow{\mu} Q \mid P'$ ;
- (HID) if  $P \xrightarrow{\mu} P'$  and the name  $a$  does not appear in  $\mu$  then  $P \setminus^n a \xrightarrow{\mu} P' \setminus^n a$ ;
- (HIDOUT) if  $P \xrightarrow{\bar{a}} P'$  then  $P \setminus^n a \xrightarrow{\bar{a}} P' \setminus^{n+1} a$ ;
- (HIDAT) if  $P \xrightarrow{\mu} P'$  and  $\mu = \theta \uplus \{a^m\}$ , where  $a$  is a name that does not appear in the label  $\theta$ , then  $P \setminus^{n+m} a \xrightarrow{\theta} P' \setminus^n a$ .

*Proof.* In each case, we have a transition of the form  $P \xrightarrow{\mu} P'$ . By definition, there are states  $\sigma$  and  $\sigma'$  such that  $P; \sigma \rightarrow P'; \sigma'$ . The property is obtained by a simple induction on this reduction (a case analysis on the last reduction rule is enough).  $\square$

We define additional transition relations used in the remainder of the paper. As usual, we denote by  $\Rightarrow$  the *weak transition relation*, that is the reflexive and transitive closure of  $\xrightarrow{\tau}$ . We denote by  $\xRightarrow{s}$  the relation  $\Rightarrow \xrightarrow{\mu} \Rightarrow$ . If  $s$  is a sequence of labels  $\mu_0 \dots \mu_n$ , we denote  $\xrightarrow{s}$  the relation such that  $P \xrightarrow{s} P'$  if and only if there is a process  $Q$  such that  $P \xrightarrow{\mu_0} Q$  and  $Q \xrightarrow{\mu_1 \dots \mu_n} P'$  (and  $\xrightarrow{s}$  is the identity relation when  $s$  is the empty sequence  $\varepsilon$ ). We also define a weak version  $\xRightarrow{s}$  of this relation in the same way. Lastly, we denote  $\xrightarrow{a^n}$  the relation  $\xrightarrow{a} \dots \xrightarrow{a}$ , the composition of  $n$  copies of  $\xrightarrow{a}$ .

**Asynchronous Bisimulation for Processes and Expressions.** Equipped with a labeled transition system, we can define the traditional (weak) bisimulation equivalence  $\approx$  between processes. This is the largest equivalence  $\mathcal{R}$  such that if  $P \mathcal{R} Q$  and  $P \xrightarrow{\mu} P'$  then  $Q \xRightarrow{\mu} Q'$  and  $P' \mathcal{R} Q'$ . Weak bisimulation can be used to prove interesting equivalences between processes. For instance, we can prove that  $(P \mid \bar{a}) \setminus^n a \approx P \setminus^{n+1} a$ . Nonetheless, a series of equivalence laws are not valid for  $\approx$ . For instance,  $\text{atom}(\text{rd } a.\text{end}) \not\approx a.\mathbf{0}$ , meaning that, whereas there are no context that separates the two processes, it is possible to test the presence of an atomic block. Also, the usual *asynchronous law* is not valid:  $a.\bar{a} \not\approx \mathbf{0}$ . To overcome these limitations, we define a weak *asynchronous bisimulation* relation, denoted  $\approx_a$ , in the style of [1].

**Definition 1 (weak asynchronous bisimulation).** A symmetric relation  $\mathcal{R}$  is a weak asynchronous bisimulation if whenever  $P \mathcal{R} Q$  then the following holds:

1. if  $P \xrightarrow{\bar{a}} P'$  then there is  $Q'$  such that  $Q \xRightarrow{\bar{a}} Q'$  and  $P' \mathcal{R} Q'$ ;
2. if  $P \xrightarrow{\theta} P'$  then there is a process  $Q'$  and a block action  $\gamma$  such that  $Q \xRightarrow{\gamma} Q'$  and  $(P' \mid \prod_{a \in (\gamma \setminus \theta)} \bar{a}) \mathcal{R} (Q' \mid \prod_{a \in (\theta \setminus \gamma)} \bar{a})$ .

We denote with  $\approx_a$  the largest weak asynchronous bisimulation.

Assume  $P \approx_a Q$  and  $P \xrightarrow{\tau} P'$ , the (derived) case for silent action entails that there is  $Q'$  and  $\theta$  such that  $Q \xRightarrow{\theta} Q'$  and  $P' \mid \prod_{a \in \theta} \bar{a} \approx_a Q'$ . If  $\theta$  is the silent action,  $\theta = \{\}$ , we recover the usual condition for bisimulation, that is  $Q \Rightarrow Q'$  and  $P' \approx_a Q'$ . If  $\theta$  is an input action,  $\theta = \{a\}$ , we recover the definition of asynchronous bisimulation of [1]. Due to the presence of block actions  $\gamma$ , the definition of  $\approx_a$  is slightly more complicated than in [1], but it is also more compact (we only have two cases) and more symmetric. Hence, we expect to be able to reuse

known methods and tools for proving the equivalence of ATCCS processes. Another indication that  $\approx_a$  is a good choice for reasoning about processes is that it is a congruence. The proof is reported in Appendix A.

**Theorem 1.** *Weak asynchronous bisimulation  $\approx_a$  is a congruence.*

We need to define a specific equivalence relation to reason on transactions. Indeed, the obvious choice that equates two expressions  $M$  and  $N$  if  $\text{atom}(M) \approx_a \text{atom}(N)$  does not lead to a congruence. For instance, we have  $(\text{rd } a.\text{wt } a.\text{end})$  equivalent to  $\text{end}$  while  $\text{atom}(\text{rd } a.\text{wt } a.\text{end orElse wt } b.\text{end}) \not\approx_a \text{atom}(\text{end orElse wt } b.\text{end})$ . The first transaction may output a message on  $b$  while the second always end silently.

We define an equivalence relation between atomic expressions  $\simeq$ , and a *weak atomic pre-order*  $\sqsubseteq$ , that relates two expressions if they end (or abort) for the same states. We also ask that equivalent expressions should perform the same changes on the global state when they end. We say that two logs  $\delta, \delta'$  have same effects, denoted  $\delta =_\sigma \delta'$  if  $\sigma \setminus \text{RD}(\delta) \uplus \text{WT}(\delta) = \sigma \setminus \text{RD}(\delta') \uplus \text{WT}(\delta')$ . We say that  $M \sqsubseteq_\sigma N$  if and only if either (1)  $(N)_{\sigma;\varepsilon} \Rightarrow (\text{retry})_{\sigma;\delta}$ ; or (2)  $(N)_{\sigma;\varepsilon} \Rightarrow (\text{end})_{\sigma;\delta}$  and  $(M)_{\sigma;\varepsilon} \Rightarrow (\text{end})_{\sigma;\delta'}$ . Similarly, we have  $M \simeq_\sigma N$  if and only if either (1)  $(M)_{\sigma;\varepsilon} \Rightarrow (\text{retry})_{\sigma;\delta}$  and  $(N)_{\sigma;\varepsilon} \Rightarrow (\text{retry})_{\sigma;\delta'}$ ; or (2)  $(M)_{\sigma;\varepsilon} \Rightarrow (\text{end})_{\sigma;\delta}$  and  $(N)_{\sigma;\varepsilon} \Rightarrow (\text{end})_{\sigma;\delta'}$  with  $\delta =_\sigma \delta'$ .

**Definition 2 (weak atomic equivalence).** *Two atomic expressions  $M, N$  are equivalent, denoted  $M \simeq N$ , if and only if  $M \simeq_\sigma N$  for every state  $\sigma$ . Similarly, we have  $M \sqsubseteq N$  if and only if  $M \sqsubseteq_\sigma N$  for every state  $\sigma$ .*

While the definition of  $\sqsubseteq$  and  $\simeq$  depend on a universal quantification over states, testing the equivalence of two expressions is not expensive. First, we can rely on a monotonicity property of reduction: if  $\sigma \subseteq \sigma'$  then for all  $M$  the effect of  $(M)_{\sigma;\delta}$  is included in those of  $(M)_{\sigma';\delta}$ . Moreover, we define a normal form for expressions later in this section (see Proposition 2) that greatly simplifies the comparison of expressions. Another indication that  $\simeq$  is a good choice of equivalence for atomic expressions is that it is a congruence. The proof is reported in Appendix A.

**Theorem 2.** *Weak atomic equivalence  $\simeq$  is a congruence.*

**Dining Philosopher.** In this example we give yet another solution to the well-known dining philosopher problem. We use atomic blocks of actions in the implementation of the system and we show that the obtained process behaves as its specification, without using backtracking and without falling into situations of deadlock. Suppose to have four philosophers,  $I = \{0, 1, 2, 3\}$  is the considered set of indexes. In what follows we write  $+$  for the sum modulo 4. Suppose  $t$  is a set of indexes corresponding to thinking philosophers, which are ready to eat; and  $e$  corresponds to eating philosophers, which are ready to think.  $P_{t;e}$  is the specification of the system, with  $t \cup e = I$ ,  $t \cap e = \emptyset$  and there isn't  $i \in I$  such that  $i, i+1 \in e$ .

$$\begin{aligned} P_{t;e} &\triangleq \sum_{i \notin t} t_i . P_{t \cup i; e - i} \\ &\quad + \sum_{\{i=0,1 \text{ if } e=\emptyset\}} \tau . (e_i . P_{t-i; i} + e_{i+2} . P_{t-(i+2); (i+2)}) \\ &\quad + \sum_{\{i \in t \mid i-1, i+1 \notin e, i+2 \in e\}} \tau . e_i . P_{t-i; e \cup i} \end{aligned}$$

The system specification will never fall into deadlocks and there can be at most two eating philosophers (with indexes  $i$  and  $i+2$ ). The actions of eating and thinking of the philosopher  $i$ ,  $e_i$  and  $t_i$ , can be observed as inputs.

A philosopher  $D_i$ , for  $i \in I$ , can be implemented as follows:

$$D_i \triangleq \text{atom}(\text{rd } c_{i-1} . \text{rd } c_i . \text{end}) . e_i . t_i . (\overline{c_{i-1}} | \overline{c_i}).$$

Process  $D_i$  attempts to get the chopsticks, on his right and left, by using an atomic block for reading  $c_{i-1}$  and  $c_i$ . If the commit of the atomic block can not be performed then at least one of its neighbors,  $D_{i-1}$  or  $D_{i+1}$  is already eating, because at least one of the chopsticks is not available, thus  $D_i$  will retry to get both chopsticks. Otherwise he can eat, thus he will acquire the chopsticks and eat by inputting  $e_i$ . After eating, he can decide to start thinking, thus he reads  $t_i$ , and after that he releases both chopsticks.

The global system is given by the parallel composition of the philosopher  $D_i$  and the output of the four chopsticks, which are hidden to observers

$$D \triangleq (D_0 | D_1 | D_2 | D_3 | \overline{c_0} | \overline{c_1} | \overline{c_2} | \overline{c_3}) \setminus^0 c_0, c_1, c_2, c_3.$$

In what follows we show that  $P_{I;\emptyset} \approx_a D$  holds. Before we need to define a useful abbreviation. Suppose  $A, B, C, D, E \subseteq \{0, 1, 2, 3\}$ , are sets of indexes such that  $A \cup B \cup C = \{0, 1, 2, 3\}$ ,  $A \cap B = A \cap C = B \cap C = \emptyset$  and  $D \cup E \subseteq \{0, 1, 2, 3\}$  with  $D \cap E = \emptyset$ . We define  $D\{A; B; C; D; E\}$  as follows:

$$\begin{aligned} D\{A; B; C; D; E\} \triangleq & (\prod_{\{i \in A\}} D_i | \prod_{\{i \in B\}} e_i . t_i . (\overline{c_{i-1}} | \overline{c_i}) \\ & | \prod_{\{i \in C\}} t_i . (\overline{c_{i-1}} | \overline{c_i}) \\ & | \prod_{\{i \in D\}} \overline{c_i}) \setminus^1 c_i | i \in E \setminus^0 c_i | i \in D. \end{aligned}$$

That is a system where the philosophers in  $A$  are in the initial state; philosophers in  $B$  are ready to eat (they have already acquired the chopsticks); philosophers in  $C$  are ready to think (they have already eaten); indexes in  $D$  correspond to available chopsticks not yet outputted; indexes in  $E$  correspond to chopsticks outputted, thus chopsticks that can be taken by some philosopher for eating.

In the following  $\mathcal{P}(S)$  represents the powerset of  $S$ .  $P_{I;\emptyset} \mathcal{R} D$  where the weak asynchronous bisimulation  $\mathcal{R}$  is defined as follows:

$$\begin{aligned} \mathcal{R} = & \{ (P_{I;\emptyset}, D\{I; \emptyset; \emptyset; I \setminus S; S\}) | S \in \mathcal{P}(I) \} \\ & \cup \{ (P_{I-i;i}, D\{I-i; \emptyset; \{i\}; \{i+1, i+2\} \setminus S; S\}) | S \in \mathcal{P}(\{i+1, i+2\}) \} \\ & \cup \{ (P_{I-i;i}, D\{\{i-1, i+1\}; \{i+2\}; \{i\}; \emptyset; \emptyset\}) \} \\ & \cup \{ (P_{\{i-1, i+1\}; \{i, i+2\}}, D\{\{i-1, i+1\}; \emptyset; \{i, i+2\}; \emptyset; \emptyset\}) \} \\ & \cup \{ ((e_i . P_{I-i;i} + e_{i+2} . P_{I-(i+2); (i+2)}), D\{\{i-1, i+1\}; \{i+2, i\}; \emptyset; \emptyset; \emptyset\}) | i = 0, 1 \} \\ & \cup \{ ((e_i . P_{I-i;i} + e_{i+2} . P_{I-(i+2); (i+2)}), D\{\{i-1, i, i+1\}; \{i+2\}; \emptyset; \{i-1, i\} \setminus S; S\}) \\ & \quad | S \in \mathcal{P}(\{i-1, i\}), i = 0, 1 \} \\ & \cup \{ ((e_i . P_{I-i;i} + e_{i+2} . P_{I-(i+2); (i+2)}), D\{\{i-1, i+1, i+2\}; \{i\}; \emptyset; \{i+1, i+2\} \setminus S; S\}) \\ & \quad | S \in \mathcal{P}(\{i+1, i+2\}), i = 0, 1 \}. \end{aligned}$$

**On the Algebraic Structure of Transactions.** The equivalence relations  $\simeq$  and  $\approx_a$  can be used to prove interesting laws of atomic expressions and processes. We list some of these laws in Table 4. Appropriate bisimulation relations which prove laws in Table 4 are reported in

Laws for atomic expressions:	
(COMM)	$\alpha.\beta.M \simeq \beta.\alpha.M$
(DIST)	$\alpha.(M \text{ orElse } N) \simeq (\alpha.M) \text{ orElse } (\alpha.N)$
(ASS)	$M_1 \text{ orElse } (M_2 \text{ orElse } M_3) \simeq (M_1 \text{ orElse } M_2) \text{ orElse } M_3$
(IDEM)	$M \text{ orElse } M \simeq M$
(ABSRT1)	$\alpha.\text{retry} \simeq \text{retry}$
(ABSRT2)	$\text{retry} \text{ orElse } M \simeq M \simeq M \text{ orElse } \text{retry}$
(ABSEND)	$\text{end} \text{ orElse } M \simeq \text{end}$
Laws for processes:	
(ASY)	$a.\bar{a} \approx_a \mathbf{0}$
(A-ASY)	$\text{atom}(\text{rd } a.\text{wt } a.\text{end}) \approx_a \mathbf{0}$
(A-1)	$\text{atom}(\text{rd } a.\text{end}) \approx_a a.\mathbf{0}$

**Table4.** Algebraic Laws of Transactions.

Appendix B. Let  $\mathcal{M}$  denotes the set of all atomic expressions. The behavioral rules for atomic expressions are particularly interesting since they exhibit a rich algebraic structure for  $\mathcal{M}$ . For instance, rules (COMM) and (DIST) state that action prefix  $\alpha.M$  is a commutative operation that distribute over `orElse`. We also have that  $(\mathcal{M}, \text{orElse}, \text{retry})$  is an idempotent semigroup with left identity `retry`, rules (ASS), (ABSRT2) and (IDEM), and that `end` annihilates  $\mathcal{M}$ , rule (ABSEND). Most of these laws appear in [19] but are not formally proved.

Actually, we can show that the structure of  $\mathcal{M}$  is close to that of a bound join-semilattice. We assume unary function symbols  $a()$  and  $\bar{a}()$  for every name  $a$  (a term  $\bar{a}(M)$  is intended to represent a prefix `wt a.M`) and use the symbols  $\sqcup, 1, 0$  instead of `orElse, end, retry`. With this presentation, the behavioral laws for atomic expression are almost those of a semilattice. By definition of  $\sqsubseteq$ , we have that  $M \sqcup M' \simeq M$  if and only if  $M \sqsupseteq M'$  and for all  $M, N$  we have  $1 \sqsubseteq M \sqcup N \sqsubseteq M \sqsubseteq 0$ .

$$\begin{aligned}
\mu(\mu'(M)) &\simeq \mu'(\mu(M)) & \mu(M \sqcup N) &\simeq \mu(M) \sqcup \mu(N) & \mu(0) &\simeq 0 \\
0 \sqcup M &\simeq M \simeq M \sqcup 0 & 1 \sqcup M &\simeq 1
\end{aligned}$$

It is possible to prove other behavioral laws to support our interpretation of `orElse` has a join. However some important properties are missing, most notably, while  $\sqcup$  is associative, it is not commutative. For instance,  $a(\bar{b}(1)) \sqcup 1 \not\simeq 1$  while  $1 \simeq 1 \sqcup a(\bar{b}(1))$ , rule (ABSEND). This observation could help improve the design of the transaction language: it will be interesting to enrich the language so that we obtain a real lattice.

**Normal Form for Transactions.** Next, we show that it is possible to rearrange an atomic expression (using behavioral laws) to put it into a simple *normal form*. This procedure can be understood as a kind of compilation that transform an expression  $M$  into a simpler form.

Informally, an atomic expression  $M$  is said to be in *normal form* if it does not contain nested `orElse` (all occurrences are at top level) and if there are no redundant branches. A redundant branch is a sequence of actions that will never be executed. For instance, the read actions in `rd a.end` are included in `rd a.rd b.end`, then the second branch in the compo-

sition  $(\text{rd } a.\text{end}) \text{ orElse } (\text{rd } a.\text{rd } b.\text{end})$  is redundant: obviously, if  $\text{rd } a.\text{end}$  fails then  $\text{rd } a.\text{rd } b.\text{end}$  cannot succeed. We overload the functions defined on logs and write  $\text{RD}(M)$  for the (multiset of) names occurring in read actions in  $M$ . We define  $\text{WT}(M)$  similarly. In what follows, we abbreviate  $(M_1 \text{ orElse } \dots \text{ orElse } M_n)$  with the expression  $\bigsqcup_{i \in 1..n} M_i$ . We say that an expression  $M$  is in *normal form* if it is of the form  $\bigsqcup_{i \in 1..n} K_i$  where for all indexes  $i, j \in 1..n$  we have: (1)  $K_i$  is a sequence of action prefixes  $\alpha_{j_1} \dots \alpha_{j_{n_i}}.\text{end}$ ; and (2)  $\text{RD}(K_i) \not\subseteq \text{RD}(K_j)$  for all  $i < j$ . Condition (1) requires the absence of nested `orElse` and condition (2) prohibits redundant branches (it also means that all branches, but the last one, has a read action). The following proposition is proved in Appendix C.

**Proposition 2.** *For every expression  $M$  there is a normal form  $M'$  such that  $M \simeq M'$ .*

Our choice of using bisimulation for reasoning about atomic transactions may appear arbitrary. We have already debated over the need to consider asynchronous bisimulation  $\approx_a$  instead of (the simple) bisimulation  $\approx$ . In the next section, we study a testing equivalence for ATCCS, more particularly an asynchronous may testing semantics [17].

## 5 May-testing semantics

Using a testing equivalence instead of bisimulation is sometimes more convenient. Nonetheless, testing equivalences have the drawback that their definition depends on a universal quantification over arbitrarily many processes. We define a may-testing equivalence for ATCCS and give an alternative characterization using a trace-based equivalence. We also expose some shortcomings of may testing related to the (folklore) fact that it cannot distinguish the points of choice in a process. Actually, we define for every atomic block  $\text{atom}(M)$  a corresponding process without transactions (but using choice) that is indistinguishable from  $\text{atom}(M)$ . The results enunciated in this section are proved in Appendix D.

We define the notion of observers and successful computations. An *observer*  $O$  is a particular type of process which does not contain atomic blocks and that can perform a distinct output  $\bar{w}$  (the success action). We denote  $\text{Obs}$  the set of all observers. A *computation* from a process  $P$  and an observer  $O$  is a sequence of transitions of the form  $P \mid O = P_0 \mid O_0 \xrightarrow{\tau} \dots \xrightarrow{\tau} P_k \mid O_k \xrightarrow{\tau} \dots$ , which is either infinite or of finite size, say  $n$ , such that  $P_n \mid O_n$  cannot evolve. A computation from  $P \mid O$  is *successful* if there is an index  $n$  such that  $O_n$  has a success action, that is  $O_n \xrightarrow{\bar{w}}$ . In this case, we say that  $P$  may  $O$ . Two processes are may testing equivalent if they have the same successful observers.

**Definition 3 (may-testing preorder).** *Given two processes  $P$  and  $Q$ , we write  $P \sqsubseteq_{\text{may}} Q$  if for every observer  $O$  in  $\text{Obs}$  we have  $P \text{ may } O$  implies  $Q \text{ may } O$ . We use  $\simeq_{\text{may}}$  to denote the equivalence obtained as the kernel of the preorder  $\sqsubseteq_{\text{may}}$ .*

Universal quantification on observers make it difficult to work with the operational definition of the may preorder. Following [7], we study a trace-based characterization for our calculus. The following preorder over traces will be used for defining the alternative characterization of the may-testing preorder.

In our setting, a *trace*  $s$  is a sequence of actions  $\mu_1 \dots \mu_n$ . (We only consider output and block actions and leave aside  $\tau$  and input actions, which are derivable). We define a preorder  $\preceq_0$  on traces as the smallest relation that satisfies the following laws.

$$\begin{aligned} \text{(TO1)} \quad s_1 s_2 &\preceq_0 s_1 \{a\} s_2 & \text{(TO2)} \quad s_1 s_2 \{a\} s_3 &\preceq_0 s_1 \{a\} s_2 s_3 \\ \text{(TO3)} \quad s_1 s_2 &\preceq_0 s_1 \{a\} \bar{a} s_2 & \text{(TO4)} \quad \{a_1, \dots, a_n\} 0 &\succeq_0 \{a_1\} \dots \{a_n\} \end{aligned}$$

Following the terminology of [7], (TO1), (TO2) and (TO3) are the laws for *deletion*, *postponement* and *annihilation* of input action. We add rule (TO4) which allows to substitute block actions with the corresponding sequences of inputs. The simulation relation  $\preceq$  is the reflexive and transitive closure of  $\preceq_0$ . The preorder  $\preceq$  is preserved by prefixing. We can now define a preorder over processes.

**Definition 4 (alternative preorder).** For processes  $P$  and  $Q$ , we set  $P \ll_{\text{may}} Q$  if for all weak transition  $P \xRightarrow{s} P'$  there is a trace  $s'$  and a process  $Q'$  such that  $s' \preceq s$  and  $Q \xRightarrow{s'} Q'$ .

We now prove coincidence of  $\ll_{\text{may}}$  and  $\sqsubseteq_{\text{may}}$ . Some definitions and preliminary results are needed. For every label  $\mu$  we define the complement  $\bar{\mu}$  such that: the complement of an output action  $\bar{a}$  is a block action  $\{a\}$  and the complement of a block action  $\{a_1, \dots, a_n\}$  is a trace  $\bar{a}_1 \dots \bar{a}_n$ . For every trace  $s = \mu_1 \dots \mu_n$ , the cotrace  $\bar{s} = \bar{\mu}_1 \dots \bar{\mu}_n$  is obtained by concatenating the complements of the actions in  $s$ . The following lemma relates the preorder  $\preceq$  with the operational semantics of processes.

**Lemma 1.** Assume that  $s' \preceq s$  and  $P \xRightarrow{\bar{s}} P'$ , then there is a process  $P''$  such that  $P \xRightarrow{s'} P''$ .

The next step is to define a special class of observers. For every trace  $s$ , we inductively define an observer  $O(s) \in \text{Obs}$  as follows:

$$O(\varepsilon) \triangleq \bar{w}, \quad O(\bar{a}s) \triangleq a.O(s), \quad O(\{a_1, \dots, a_n\}s) \triangleq \left( \prod_{i \in 1..n} \bar{a}_i \right) | O(s)$$

The following property shows that the sequence of visible actions from  $O(s)$  is related to traces simulated by  $s$ .

**Lemma 2.** Consider two traces  $s$  and  $r$ . If there is a process  $Q$  such that  $O(s) \xRightarrow{\bar{r}} \bar{w} Q$  then  $r \preceq s$ .

We can now prove a full abstraction theorem between may testing  $\sqsubseteq_{\text{may}}$  and the alternative preorder  $\ll_{\text{may}}$ .

**Theorem 3.** For all processes  $P$  and  $Q$ , we have  $P \sqsubseteq_{\text{may}} Q$  if and only if  $P \ll_{\text{may}} Q$ .

Next, we show that may-testing semantics is not precise enough to tell apart atomic transactions from sequences of input actions. We consider an atomic expression  $M$  in normal form. Assume  $M = \sqcup_{i \in 1..n} M_i$ , the following lemma state that the observing behavior of  $M$  is obtained by considering, for every branch  $K_i$ , a transition labeled by the block action containing  $\text{RD}(K_i)$  followed by output transitions on the names in  $\text{WT}(K_i)$ .

**Lemma 3.** Assume  $M = \sqcup_{i \in 1..n} K_i$  is an expression in normal form. For every index  $i$  in  $\{1, \dots, n\}$  we have  $\text{atom}(M); \sigma_i \rightarrow^* \{(\text{end})_{\sigma_i; \delta}\}_M; \sigma_i$  where  $\sigma_i = \text{RD}(K_i) = \text{RD}(\delta)$  and  $\text{WT}(\delta) = \text{WT}(K_i)$ .

As a corollary of Lemma 3, we obtain that the possible behavior of  $\text{atom}(M)$  can be described as  $\text{atom}(M) \xRightarrow{\sigma_i} \prod_{b \in \text{WT}(K_i)} \bar{b}$  for every  $i \in 1..n$ , where  $\sigma_i$  is the multiset  $\text{RD}(K_i)$ .

We now prove that for every atomic transaction  $\text{atom}(M)$  there is a CCS process  $\llbracket M \rrbracket$  that is may-testing equivalent to  $M$ . By CCS process, we intend a term of ATCCS without atomic transaction that may include occurrences of the choice operator  $P + Q$ . By Proposition 2, we can assume that  $M$  is in normal form, that is  $M = \sqcup_{i \in 1..n} K_i$ . The interpretation of a sequence of actions  $K = \alpha_1 \dots \alpha_n \cdot \text{end}$  is the process  $\llbracket K \rrbracket = a_1 \dots a_k \cdot (\bar{b}_1 \mid \dots \mid \bar{b}_l)$  where  $\{a_1, \dots, a_k\} =$



$\text{RD}(K)$  and  $\{b_1, \dots, b_l\} = \text{WT}(K)$ . (In particular we have  $\llbracket \text{end} \rrbracket = \mathbf{0}$ .) The translated of  $M$ , denoted  $\llbracket M \rrbracket$ , is the process  $\llbracket K_1 \rrbracket + \dots + \llbracket K_n \rrbracket$ . The following theorem proves that may-testing semantics is not able to distinguish the behavior of an atomic process from the behavior of its translation, which means that may-testing is blind to the presence of transactions.

**Proposition 3.** *For every expression  $M$  in normal form we have  $\text{atom}(M) \simeq_{\text{may}} \llbracket M \rrbracket$ .*

We observe that a process  $\llbracket M \rrbracket$  is a choice between processes of the form  $a.P$  or  $(\prod_{i \in I} \overline{b_i})$ . Therefore, using internal choice and a slightly more convoluted encoding, it is possible to use only input guarded choice  $a.P + b.Q$  in place of full choice in the definition of  $\llbracket M \rrbracket$ .

## 6 Future and Related Works

There is a long history of works that try to formalize the notions of transactions and atomicity, and a variety of approaches to tackle this problem. We review some of these works that are the most related to ours.

We can list several works that combine ACID transactions with process calculi. Gorrieri et al [18] have modeled concurrent systems with atomic behaviors using an extension of CCS. They use a two-level transition systems (a high and a low level) where high actions are decomposed into atomic sequences of low actions. To enforce isolation, atomic sequences must go into a special invisible state during all their execution. Contrary to our model, this work does not follow an optimistic approach: sequences are executed sequentially, without interleaving with other actions, as though in a critical section. Another related calculus is RCCS, a reversible version of CCS [15,16] based on an earlier notion of process calculus with backtracking [3]. In RCCS, each process has access to a log of its synchronization's history and may always wind back to a previous state. This calculus guarantees the ACD properties of transactions (isolation is meaningless since RCCS do not use a shared memory model). Finally, a framework for specifying the semantics of transactions in an object calculus is given in [26]. The framework is parametrized by the definition of a transactional mechanism and allows the study of multiple models, such as e.g. the usual lock-based approach. In this work, STM is close to a model called *versioning semantics*. Like in our approach, this model is based on the use of logs and is characterized by an optimistic approach where log consistency is checked at commit time. Fewer works consider behavioral equivalences for transactions. A foundational work is [5], that gives a theory of transactions specifying atomicity, isolation and durability in the form of an equivalence relation on processes, but it provides no formal proof system.

Linked to the upsurge of works on Web Services (and on long running Web transactions), a larger body of works is concerned with formalizing *compensating transactions*. In this context, each transactive block of actions is associated with a compensation (code) that has to be run if a failure is detected. The purpose of compensation is to undo most of the visible actions that have been performed and, in this case, atomicity, isolation and durability are obviously violated. We give a brief survey of works that formalize compensable processes using process calculi. These works are of two types: (1) *interaction based compensation* [6,8,21], which are extensions of process calculi (like  $\pi$  or join-calculus) for describing transactional choreographies where composition take place dynamically and where each service describes its possible interactions and compensations; (2) *compensable flow composition* [9,11,12,13], where ad hoc process algebras are designed from scratch to describe the possible flow of control among services. These calculi are oriented towards the orchestration of services and service failures. This second approach is also followed in [2,4] where two frameworks for composing transactional services are presented.

The study of ATCCS is motivated by our objective to better understand the semantics of the STM model. Obtaining a suitable behavioral equivalence for atomic expression is a progress for the verification of concurrent applications that use STM. However, we can imagine using our calculus for other purposes. An interesting problem is to develop an approach merging atomic and compensating transactions. A first step in this direction is to enrich our language and allow the parallel composition of atomic expressions and the nesting of transactions. We are currently working on this problem. Another area for research stems from our observation (see Section 4) that the algebraic structure of atomic expressions is lacking interesting property. Indeed, it will be interesting to enrich the language of expressions in order to obtain a real lattice. The addition of a symmetric choice operator for atomic expressions may be a solution, but it could introduce unwanted nondeterminism in the evaluation of transactions.

## References

1. R. Amadio, I. Castellani and D. Sangiorgi. On Bisimulations for the Asynchronous  $\pi$ -Calculus. *Theoretical Computer Science*, 195(2):291–324, 1998.
2. D. Berardi, D. Calvanese, G. De Giacomo, R. Hull and M. Mecella. Automatic Composition of Transition-Based Web Services with Messaging. In *Proc. of VLDB*, 2005.
3. J.A. Bergstra, A. Ponse and J.J. van Wamel. Process Algebra with Backtracking. In *Proc. of REX Workshop*, LNCS 803, 1994.
4. S. Bhiri, O. Perrin and C. Godart. Ensuring Required Failure Atomicity of Composite Web Services. In *Proc. of WWW*, ACM Press, 2005.
5. A.P. Black, V. Cremet, R. Guerraoui and M. Odersky. An Equational Theory for Transactions. In *Proc. of FSTTCS*, LNCS 2914, 2003.
6. L. Bocchi, C. Laneve and G. Zavattaro. A Calculus for Long Running Transactions. In *Proc. of FMOODS*, LNCS 2884, 2003.
7. M. Boreale, R. De Nicola and R. Pugliese. Trace and Testing Equivalence on Asynchronous Processes. *Information and Computation*, 172(2): 139–164, 2002.
8. R. Bruni, H.C. Melgratti and U. Montanari. Nested Commits for Mobile Calculi: extending Join. In *Proc. of IFIP TCS*, 563–576, 2004.
9. R. Bruni, H.C. Melgratti and U. Montanari. Theoretical Foundations for Compensations in Flow Composition Languages. In *Proc. of POPL*, ACM Press, 209–220, 2005.
10. N. Busi, R. Gorrieri, G. Zavattaro. A Process Algebraic View of Linda Coordination Primitives. *Theoretical Computer Science*, 192(2):167–199, 1998.
11. M.J. Butler, C.A.R. Hoare and C. Ferreira. A Trace Semantics for Long-Running Transactions. In *Proc. of 25 Years Communicating Sequential Processes*, LNCS 3525, 2004.
12. M.J. Butler, C.A.R. Hoare and C. Ferreira. An Operational Semantics for StAC, a Language for Modeling Long Running Transactions. In *Proc. of COORDINATION*, LNCS 2949, 2004.
13. M.J. Butler, C. Ferreira and M.Y. Ng. Precise Modeling of Compensating Business Transactions and its Application to BPEL. In *J. UCS*, 11:712–743, 2005.
14. T. Chothia and D. Duggan. Abstractions for Fault-Tolerant Global Computing. *Theoretical Computer Science*, 322(3):567–613, 2004.
15. V. Danos and J. Krivine. Reversible Communicating System. In *Proc. of CONCUR*, LNCS 3170, 2004.
16. V. Danos and J. Krivine. Transactions in RCCS. In *Proc. of CONCUR*, LNCS 3653, 2005.
17. R. De Nicola and M.C.B. Hennessy. Testing Equivalence for Processes. *Theoretical Computer Science*, 34:83–133, 1984.
18. R. Gorrieri, S. Marchetti and U. Montanari. A<sup>2</sup>CCS: Atomic Actions for CCS. *Theoretical Computer Science*, 72(2-3):203–223, 1990.
19. T. Harris, S. Marlow, S.P. Jones and M. Herlihy. Composible Memory Transactions. In *Proc. of PPOPP*, ACM Press, 48–60, 2005.

20. M. Herlihy, J.E. Moss. Transactional Memory: Architectural Support for Lock-Free Data Structures In *Proc. of International Symposium on Computer Architecture*, 1993.
21. C. Laneve and G. Zavattaro. Foundations of Web Transactions. In *Proc. of FoSSaCS*, LNCS 3441, 2005.
22. R. Milner. Calculi for Synchrony and Asynchrony. *Theoretical Computer Science*, 25:267–310, 1983.
23. R. Milner. A Calculus of Communicating Systems. Springer-Verlag, 1982.
24. C. Palamidessi. Comparing the Expressive Power of the Synchronous and the Asynchronous pi-calculus. *Mathematical Structures in Computer Science*, 13(5), 2003.
25. N. Shavit and D. Touitou. Software Transactional Memory. In *Proc. of Principles of Distributed Computing*, ACM Press, 1995.
26. J. Vitek, S. Jagannathan, A. Welc and A.L. Hosking. A semantic Framework for Designer Transactions. In *Proc. of ESOP*, LNCS 2986, 2004.

## A Proofs of Section 4

Before proving the validity of Theorem 1 and Theorem 2, it is necessary to present some preliminary results.

The following proposition reminds an important property of asynchronous calculi: no behavior causally depends on the execution of output actions. Relation  $\sim$  stands for the usual strong bisimulation relation (see e.g. [23]).

**Proposition A1**  $P \xrightarrow{\bar{a}} P'$  implies  $P \sim P' | \bar{a}$ .

*Proof.* By observing that outputs are non-blocking actions, a suitable strong bisimulation can be defined.  $\square$

As direct consequences of the previous proposition, we get the results enunciated in the following lemma: (1) output actions can always be delayed and (2) a diamond property involving outputs.

**Lemma A1** Let  $\mu$  be a generic action ( $\mu ::= \bar{b} | \theta | \tau$ ):

1.  $P \xrightarrow{\bar{a}} P'$  implies  $P \xrightarrow{\mu} \bar{a} P'$ ; similarly  $P \xrightarrow{\bar{a}} P'$  implies  $P \xrightarrow{\mu} \bar{a} P'$ ;
2.  $P \xrightarrow{\bar{a}} P'$  and  $P \xrightarrow{\mu} P''$  imply that there is a  $P'''$  such that  $P' \xrightarrow{\mu} P'''$  and  $P'' \xrightarrow{\bar{a}} P'''$ ; similarly  $P \xrightarrow{\bar{a}} P'$  and  $P \xrightarrow{\mu} P''$  imply that there is a  $P'''$  such that  $P' \xrightarrow{\mu} P'''$  and  $P'' \xrightarrow{\bar{a}} P'''$ .

*Proof.* By Proposition A1.  $\square$

The following propositions enunciate two relevant properties of the hiding operator.

**Proposition A2**  $(P | \bar{a}) \setminus^n b \approx_a (P \setminus^n b | \bar{a})$  if  $a \neq b$ .

*Proof.* By Proposition 1 (HID), and definition of  $\xrightarrow{\mu}$ .  $\square$

**Proposition A3**  $(P | \bar{a}) \setminus^n a \approx_a P \setminus^{n+1} a$ .

*Proof.* It is enough to note that  $(P | \bar{a}) \setminus^n a \xrightarrow{\tau} P \setminus^{n+1} a$ , Proposition 1 (HIDAT).  $\square$

In the following propositions we prove that  $\approx_a$  and  $\preceq$  are closed under contexts; as a consequence we obtain that both are congruences.

**Proposition A4**  $P \approx_a Q$  implies  $\forall a : a.P \approx_a a.Q$ .

*Proof.* It is enough to show that the relation  $\mathcal{R} = \approx_a \cup \{(a.P, a.Q)\}$  is a weak asynchronous bisimulation.  $\square$

**Proposition A5**  $P \approx_a Q$  implies  $\forall a : *a.P \approx_a *a.Q$ .

*Proof.* It is enough to show that the relation

$$\mathcal{R} = \{((\prod_i P_i^{n_i} | *a.P), (\prod_i Q_i^{n_i} | *a.Q)) \mid n_i \geq 0, (P_i, Q_i) \in \approx_a\}$$

where  $P^n$  is a shorthand for the parallel composition of  $n$  copies of  $P$  and  $\prod_i P_i$  stands for  $P_1 | \dots | P_n | \dots$ , is a weak asynchronous bisimulation up to  $\sim$ .

The proof proceeds as usual, by showing that every transition of the left term can be matched by a transition of the right one (and vice-versa), and the pair composed by the arrival processes is in  $\mathcal{R}$ . The proof is straightforward by a simple case analysis of transitions, as defined in Proposition 1. The most involved case is when a communication occurs between two subprocesses, let's say  $P_j$  and  $P_k$ . Suppose  $P_j \xrightarrow{a} P'_j$  and  $P_k \xrightarrow{\bar{a}} P'_k$ . This means that, by Proposition 1 (COM):

$$(\prod_i P_i^{n_i} \mid *a.P) \xrightarrow{\tau} (\prod_{i \neq j,k} P_i^{n_i} \mid P_j^{n_j-1} \mid P_k^{n_k-1} \mid P'_j \mid P'_k \mid *a.P) = R_1.$$

By  $P_k \approx_a Q_k$  we know that  $Q_k \xRightarrow{\bar{a}} Q'_k$  with  $P'_k \approx_a Q'_k$ . We distinguish the following cases for  $Q_j$ :

$Q_j \xRightarrow{a} Q'_j$ : in this case  $Q'_j \approx_a P'_j$  and, by Proposition 1 (COM):

$$(\prod_i Q_i^{n_i} \mid *a.Q) \xrightarrow{\tau} (\prod_{i \neq j,k} Q_i^{n_i} \mid Q_j^{n_j-1} \mid Q_k^{n_k-1} \mid Q'_j \mid Q'_k \mid *a.Q) = R_2$$

and  $(R_1, R_2) \in \mathcal{R}$  by definition of  $\mathcal{R}$ .  
 $Q_j \xRightarrow{\theta} Q'_j$ : this means that, by Proposition 1 (PAR):

$$(\prod_i Q_i^{n_i} \mid *a.Q) \xRightarrow{\theta} (\prod_{i \neq j} Q_i^{n_i} \mid Q_j^{n_j-1} \mid Q'_j \mid *a.Q) = R_2$$

and we have to show that  $R_1 \mid \prod_{b \in \theta} \bar{b} \approx_a R_2$ . We distinguish two cases:

$a \in \theta$ : from  $P_j \approx_a Q_j$  we obtain that  $P'_j \mid \prod_{b \in \theta \setminus a} \bar{b} \approx_a Q'_j$ . Moreover, remembering that  $P'_k \approx_a Q'_k$ , we have (by definition of  $\mathcal{R}$ ):

$$(\prod_{i \neq j,k} P_i^{n_i} \mid P_j^{n_j-1} \mid P_k^{n_k-1} \mid P'_j \mid \prod_{b \in \theta \setminus a} \bar{b} \mid P'_k \mid *a.P) \mathcal{R} (\prod_{i \neq j,k} Q_i^{n_i} \mid Q_j^{n_j-1} \mid Q_k^{n_k-1} \mid Q'_j \mid Q'_k \mid *a.Q)$$

but  $\bar{a} \approx_a \bar{a}$ , thus we also have (again by definition of  $\mathcal{R}$ )

$$(\prod_{i \neq j,k} P_i^{n_i} \mid P_j^{n_j-1} \mid P_k^{n_k-1} \mid P'_j \mid \prod_{b \in \theta \setminus a} \bar{b} \mid P'_k \mid \bar{a} \mid *a.P) \mathcal{R} (\prod_{i \neq j,k} Q_i^{n_i} \mid Q_j^{n_j-1} \mid Q_k^{n_k-1} \mid Q'_j \mid Q'_k \mid \bar{a} \mid *a.Q)$$

by Proposition A1,  $\bar{a} \mid Q'_k \sim Q_k$ , thus

$$(\prod_{i \neq j,k} P_i^{n_i} \mid P_j^{n_j-1} \mid P_k^{n_k-1} \mid P'_j \mid \prod_{b \in \theta} \bar{b} \mid P'_k \mid *a.P) \mathcal{R} \sim (\prod_{i \neq j} Q_i^{n_i} \mid Q_j^{n_j-1} \mid Q'_j \mid *a.Q).$$

$a \notin \theta$ : from  $P_j \approx_a Q_j$  we obtain that  $P'_j \mid \prod_{b \in \theta} \bar{b} \approx_a Q'_j \mid \bar{a}$ . Moreover, remembering that  $P'_k \approx_a Q'_k$ , we have (by definition of  $\mathcal{R}$ ):

$$(\prod_{i \neq j,k} P_i^{n_i} \mid P_j^{n_j-1} \mid P_k^{n_k-1} \mid P'_j \mid \prod_{b \in \theta} \bar{b} \mid P'_k \mid *a.P) \mathcal{R} (\prod_{i \neq j,k} Q_i^{n_i} \mid Q_j^{n_j-1} \mid Q_k^{n_k-1} \mid Q'_j \mid \bar{a} \mid Q'_k \mid *a.Q)$$

by Proposition A1,  $\bar{a} \mid Q'_k \sim Q_k$ , thus

$$(\prod_{i \neq j,k} P_i^{n_i} \mid P_j^{n_j-1} \mid P_k^{n_k-1} \mid P'_j \mid \prod_{b \in \theta} \bar{b} \mid P'_k \mid *a.P) \mathcal{R} \sim (\prod_{i \neq j} Q_i^{n_i} \mid Q_j^{n_j-1} \mid Q'_j \mid *a.Q).$$

□

**Proposition A6**  $P \approx_a Q$  implies  $\forall R: P|R \approx_a Q|R$ .

*Proof.* The proof proceeds by showing that the relation

$$\mathcal{R} = \{(P|R, Q|R) \mid (P, Q) \in \approx_a\}$$

is a weak asynchronous bisimulation up to  $\sim$ .

Suppose  $P|R \xrightarrow{\mu} S$ ; by applying Proposition 1, we can distinguish the following cases obtained by applying Proposition 1 (PAR) or (COM):

$R \xrightarrow{\mu} R'$ :  $S = P|R'$ ; by Proposition 1 (PAR),  $Q|R \xrightarrow{\mu} Q|R'$  and  $(P|R')\mathcal{R}(Q|R')$  by definition of  $\mathcal{R}$ ;

$P \xrightarrow{\bar{a}} P'$ :  $\mu = \bar{a}$  and  $S = P'|R$ . By  $P \approx_a Q$  we have  $Q \xrightarrow{\bar{a}} Q'$  with  $P' \approx_a Q'$ . By Proposition 1 (PAR),  $Q|R \xrightarrow{\bar{a}} Q'|R$  and  $(P'|R)\mathcal{R}(Q'|R)$  by definition of  $\mathcal{R}$ ;

$P \xrightarrow{\theta} P'$ :  $\mu = \theta$  and  $S = P'|R$ . By  $P \approx_a Q$  we have  $Q \xrightarrow{\theta} Q'$  and  $(P'|\prod_{a \in \theta \setminus \theta'} \bar{a}) \approx_a (Q'|\prod_{a \in \theta \setminus \theta'} \bar{a})$ .

By Proposition 1 (PAR),  $Q|R \xrightarrow{\theta'} Q'|R$  and  $(P'|\prod_{a \in \theta' \setminus \theta} \bar{a}|R)\mathcal{R}(Q'|\prod_{a \in \theta' \setminus \theta} \bar{a}|R)$  follows from  $(P'|\prod_{a \in \theta' \setminus \theta} \bar{a}) \approx_a (Q'|\prod_{a \in \theta' \setminus \theta} \bar{a})$  and definition of  $\mathcal{R}$ ;

$P \xrightarrow{\bar{a}} P'$  and  $R \xrightarrow{\bar{a}} R'$ :  $\mu = \tau$  and  $S = P'|R'$ .  $P \approx_a Q$  implies  $Q \xrightarrow{\bar{a}} Q'$  and  $P' \approx_a Q'$ . By Proposition 1 (COM),  $Q|R \Rightarrow Q'|R'$  and, by definition of  $\mathcal{R}$ ,  $(P'|R')\mathcal{R}(Q'|R')$ ;

$P \xrightarrow{\{a\}} P'$  and  $R \xrightarrow{\bar{a}} R'$ :  $\mu = \tau$  and  $S = P'|R'$ .  $P \approx_a Q$  implies that  $Q \xrightarrow{\bar{a}} Q'$ . We consider the following cases by distinguishing the possible values of  $\theta$ :

$\theta = \{a\}$ : in this case  $P' \approx_a Q'$ . By Proposition 1 (COM),  $Q|R \Rightarrow Q'|R'$  and, by definition of  $\mathcal{R}$ ,  $(P'|R')\mathcal{R}(Q'|R')$ ;

**otherwise:**  $Q|R \xrightarrow{\bar{a}} Q'|R$  by Proposition 1 (PAR); we have to prove that  $P'|R'|\prod_{b \in \theta} \bar{b} \approx Q'|R$ . We distinguish the following cases:

$a \in \theta$ : from  $P \approx_a Q$  we obtain  $P'|\prod_{b \in \theta \setminus a} \bar{b} \approx_a Q'$ , by definition of  $\mathcal{R}$ :

$$P'|\prod_{b \in \theta \setminus a} \bar{b}|R \approx Q'|R$$

and by Proposition A1,  $R \sim R'|\bar{a}$ , thus

$$P'|R'|\prod_{b \in \theta} \bar{b} \sim_a Q'|R;$$

$a \notin \theta$ : from  $P \approx_a Q$  we obtain  $P'|\prod_{b \in \theta} \bar{b} \approx_a Q'|\bar{a}$ , by definition of  $\mathcal{R}$ :

$$P'|\prod_{b \in \theta} \bar{b}|R' \approx_a Q'|\bar{a}|R'$$

and by Proposition A1,  $R \sim R'|\bar{a}$ , thus

$$P'|R'|\prod_{b \in \theta} \bar{b} \sim_a Q'|R.$$

□

**Proposition A7**  $P \approx_a Q$  implies  $\forall a, n \geq 0: P \setminus^n a \approx_a Q \setminus^n a$ .

*Proof.* The proof proceeds by showing that the relation:

$$\mathcal{R} = \{(P_i \setminus^{n+i} a, Q_j \setminus^{n+j} a) \mid n \geq 0, (P, Q) \in \approx_a, P \xrightarrow{\bar{a}^i} P_i, Q \xrightarrow{\bar{a}^j} Q_j\}$$

is a weak asynchronous bisimulation up to  $\sim$ . We distinguish the following cases:

(HID):  $P_i \setminus^{n+i} a \xrightarrow{\mu} P'_i \setminus^{n+i} a$  is derived by  $P_i \xrightarrow{\mu} P'_i$ , if  $a$  not appears in  $\mu$ . By Lemma A1 (1),  $P \xrightarrow{\bar{a}} P_i \xrightarrow{\mu} P'_i$  implies  $P \xrightarrow{\mu} P' \xrightarrow{\bar{a}} P'_i$ . From  $P \approx_a Q$  we obtain  $Q \xrightarrow{\mu} Q'$  with  $P' \approx_a Q'$  and by  $Q \xrightarrow{\bar{a}} Q_j$  and Lemma A1 (2),  $Q' \xrightarrow{\bar{a}} Q'_j$  and  $Q_j \xrightarrow{\mu} Q'_j$ ; by Proposition 1 (HID),  $Q_j \setminus^{n+j} a \xrightarrow{\mu} Q'_j \setminus^{n+j} a$ . Finally,  $(P'_i \setminus^{n+i} a) \mathcal{R} (Q'_j \setminus^{n+j} a)$  because of  $P' \approx_a Q'$ ,  $P' \xrightarrow{\bar{a}} P'_i$ ,  $Q' \xrightarrow{\bar{a}} Q'_j$  and definition of  $\mathcal{R}$ ;

(HIDAT):  $P_i \setminus^{n+i} a \xrightarrow{\theta} P'_i \setminus^{n'} a$  is derived by  $P_i \xrightarrow{\theta'} P'_i$  with  $\theta' = \theta \uplus a^m$  and  $n' = n + i - m$ . By Lemma A1 (1),  $P \xrightarrow{\bar{a}} P_i \xrightarrow{\theta} P'_i$  implies  $P \xrightarrow{\theta} P' \xrightarrow{\bar{a}} P'_i$ . By  $P \approx_a Q$ ,  $Q \xrightarrow{\bar{a}} Q'$  with  $(P' \mid \prod_{b \in \gamma \setminus \theta'} \bar{b}) \approx_a (Q' \mid \prod_{b \in \theta' \setminus \gamma} \bar{b})$ . Suppose  $\gamma' = \gamma \uplus a^{m'}$  and, without loss of generality, that  $m' > m$ . We can rewrite  $P' \mid \prod_{b \in \gamma' \setminus \theta'} \bar{b}$  as  $P' \mid \bar{a}^{m'-m} \mid \prod_{b \in \gamma \setminus \theta} \bar{b}$  and  $Q' \mid \prod_{b \in \theta' \setminus \gamma'} \bar{b}$  as  $Q' \mid \prod_{b \in \theta \setminus \gamma} \bar{b}$ , thus

$$(P' \mid \bar{a}^{m'-m} \mid \prod_{b \in \gamma \setminus \theta} \bar{b}) \approx_a (Q' \mid \prod_{b \in \theta \setminus \gamma} \bar{b}).$$

Moreover, by Lemma A1 (2),  $Q \xrightarrow{\gamma'} Q'$  and  $Q \xrightarrow{\bar{a}} Q_j$  imply  $Q_j \xrightarrow{\gamma'} Q'_j$  and  $Q' \xrightarrow{\bar{a}} Q'_j$ ; by Proposition 1 (HIDAT),  $Q_j \setminus^{n+j} a \xrightarrow{\gamma'} Q'_j \setminus^{n+j-m'} a$ .

We have to relate the processes  $P'_i \setminus^{n+i-m} a \mid \prod_{b \in \gamma \setminus \theta} \bar{b}$  and  $Q'_j \setminus^{n+j-m'} a \mid \prod_{b \in \theta \setminus \gamma} \bar{b}$ . By Proposition 1 (HIDOUT),  $(P' \mid \bar{a}^{m'-m} \mid \prod_{b \in \gamma \setminus \theta} \bar{b}) \setminus^{n-m'} a \xrightarrow{\tau} (P'_i \mid \prod_{b \in \gamma \setminus \theta} \bar{b}) \setminus^{n+i-m} a$  and  $(Q' \mid \prod_{b \in \theta \setminus \gamma} \bar{b}) \setminus^{n-m'} a \xrightarrow{\tau} (Q'_j \mid \prod_{b \in \theta \setminus \gamma} \bar{b}) \setminus^{n+j-m'} a$ ; thus from  $(P' \mid \bar{a}^{m'-m} \mid \prod_{b \in \gamma \setminus \theta} \bar{b}) \approx_a (Q' \mid \prod_{b \in \theta \setminus \gamma} \bar{b})$  we obtain  $((P'_i \mid \prod_{b \in \gamma \setminus \theta} \bar{b}) \setminus^{n+i-m} a) \mathcal{R} ((Q'_j \mid \prod_{b \in \theta \setminus \gamma} \bar{b}) \setminus^{n+j-m'} a)$ , that is  $P'_i \setminus^{n+i-m} a \mid \prod_{b \in \gamma \setminus \theta} \bar{b} \sim \mathcal{R} \sim Q'_j \setminus^{n+j-m'} a \mid \prod_{b \in \theta \setminus \gamma} \bar{b}$ , by Proposition A2.

(HIDOUT):  $P_i \setminus^{n+i} a \xrightarrow{\tau} P'_i \setminus^{n+i+1} a$  is derived by  $P_i \xrightarrow{\bar{a}} P'_i$ ;  $P'_i = P_{i+1}$  and by definition of  $\mathcal{R}$  we have  $(P_{i+1} \setminus^{n+i+1} a) \mathcal{R} (Q_j \setminus^{n+j} a)$ .  $\square$

**Proposition A8** Suppose  $\alpha = \text{rd } a$  or  $\alpha = \text{wt } a$ . If  $M \preceq N$  then  $\alpha.M \preceq \alpha.N$ .

*Proof.* Consider the case  $\alpha = \text{rd } a$ . It suffices to show that  $\mathcal{R} \subseteq \preceq$ , where

$$\begin{aligned} \mathcal{R} = & \{((\text{rd } a.M)_{\sigma;\varepsilon}, (\text{rd } a.N)_{\sigma;\varepsilon}), ((\text{retry})_{\sigma;\varepsilon}, (\text{retry})_{\sigma;\varepsilon})\} \cup \\ & \{((M')_{\sigma;\text{rd } a.\delta}, (N')_{\sigma;\text{rd } a.\delta'}) \mid ((M')_{\sigma\setminus\{a\};\delta}, (N')_{\sigma\setminus\{a\};\delta'}) \in \preceq, \\ & (M)_{\sigma\setminus\{a\};\varepsilon} \Rightarrow (M')_{\sigma\setminus\{a\};\delta} \text{ and } (N)_{\sigma\setminus\{a\};\varepsilon} \Rightarrow (N')_{\sigma\setminus\{a\};\delta'}\}. \end{aligned}$$

Note that  $M \preceq N$  implies  $\delta =_{\sigma\setminus\{a\}} \delta'$ , thus  $\text{rd } a.\delta =_{\sigma} \text{rd } a.\delta'$ .  $\square$

**Proposition A9** If  $M_1 \preceq N_1$  and  $M_2 \preceq N_2$  then  $M_1 \text{ orElse } M_2 \preceq N_1 \text{ orElse } N_2$ .

*Proof.* It suffices to show that  $\mathcal{R} \subseteq \preceq$ , where

$$\begin{aligned} \mathcal{R} = & \{((M_1 \text{ orElse } M_2)_{\sigma;\varepsilon}, (N_1 \text{ orElse } N_2)_{\sigma;\varepsilon})\} \\ & \cup \{((A \text{ orElse } B), (C \text{ orElse } D)) \mid (M_1)_{\sigma;\varepsilon} \Rightarrow A, (M_2)_{\sigma;\varepsilon} \Rightarrow B, (N_1)_{\sigma;\varepsilon} \Rightarrow C, \\ & (N_2)_{\sigma;\varepsilon} \Rightarrow D, (A, C) \in \preceq, (B, D) \in \preceq\} \\ & \cup \{(B, D) \mid (M_1)_{\sigma';\varepsilon} \Rightarrow (\text{retry})_{\sigma';\delta}, (N_1)_{\sigma';\varepsilon} \Rightarrow (\text{retry})_{\sigma';\delta'}, (M_2)_{\sigma';\varepsilon} \Rightarrow B, \\ & (N_2)_{\sigma';\varepsilon} \Rightarrow D, (B, D) \in \preceq\} \\ & \cup \{((\text{end})_{\sigma'';\delta}, (\text{end})_{\sigma'';\delta'}) \mid (M_1)_{\sigma'';\varepsilon} \Rightarrow (\text{end})_{\sigma'';\delta}, (N_1)_{\sigma'';\varepsilon} \Rightarrow (\text{end})_{\sigma'';\delta'}\}. \end{aligned}$$

Note that  $M_i \preceq N_i$ , for  $i = 1, 2$ , ensures that, in case of successful termination, the resulting logs have the same effects.  $\square$

Weak atomic bisimulation entails weak asynchronous bisimulation, but the inverse does not hold. E.g.  $\text{atom}(\text{rd } a.\text{wt } a.\text{end}) \approx_a \text{atom}(\text{end})$  but  $\text{rd } a.\text{wt } a.\text{end} \not\approx \text{end}$ .

**Proposition A10**  $M \preceq N$  implies  $\text{atom}(M) \approx_a \text{atom}(N)$ .

*Proof.* By contradiction, suppose that  $\text{atom}(M) \not\approx_a \text{atom}(N)$ . This means that there is a  $\delta$  such that  $\text{atom}(M) \xrightarrow{\text{RD}(\delta)} P$ , with  $P = \prod_{b \in \text{WT}(\delta)} \bar{b}$ , and for every  $\delta'$  such that  $\text{atom}(N) \xrightarrow{\text{RD}(\delta')} Q$ , with  $Q = \prod_{b \in \text{WT}(\delta')} \bar{b}$ , we have  $(P \mid \prod_{b \in (\text{RD}(\delta') \setminus \text{RD}(\delta))} \bar{b}) \not\approx_a (Q \mid \prod_{b \in (\text{RD}(\delta) \setminus \text{RD}(\delta'))} \bar{b})$ . This means that there is an  $a$  such that  $(P \mid \prod_{b \in (\text{RD}(\delta') \setminus \text{RD}(\delta))} \bar{b}) \xrightarrow{a}$  and  $(Q \mid \prod_{b \in (\text{RD}(\delta) \setminus \text{RD}(\delta'))} \bar{b}) \not\xrightarrow{a}$  (or vice versa).

By rules (ATPASS) and (ATOK) and definition of  $\xrightarrow{\mu}$ ,  $\text{atom}(M) \xrightarrow{\text{RD}(\delta)} P$  implies that there is a  $\sigma$  such that  $(M)_{\sigma;\varepsilon} \Rightarrow (\text{end})_{\sigma;\delta} \xrightarrow{\text{RD}(\delta)} P$ . By definition of  $\preceq$  there is a  $\delta''$  such that  $(N)_{\sigma;\varepsilon} \Rightarrow (\text{end})_{\sigma;\delta''}$ , with  $\delta =_{\sigma} \delta''$ , that is  $\sigma \setminus \text{RD}(\delta) \uplus \text{WT}(\delta) = \sigma \setminus \text{RD}(\delta'') \uplus \text{WT}(\delta'')$ . Thus by rules (ATPASS) and (ATOK) and Proposition 1  $\text{atom}(N) \xrightarrow{\text{RD}(\delta'')} Q$  with  $Q = \prod_{b \in \text{WT}(\delta'')} \bar{b}$ .

Suppose  $P = \prod_{b \in \text{WT}(\delta)} \bar{b} \xrightarrow{a}$ ; this means that  $a \in \text{WT}(\delta)$ . From  $\sigma \setminus \text{RD}(\delta) \uplus \text{WT}(\delta) = \sigma \setminus \text{RD}(\delta'') \uplus \text{WT}(\delta'')$  we obtain  $\text{WT}(\delta) = \text{WT}(\delta'') \uplus \text{RD}(\delta) \setminus \text{RD}(\delta'')$ , hence or  $Q = \prod_{b \in \text{WT}(\delta'')} \bar{b} \xrightarrow{a}$  or  $\prod_{b \in (\text{RD}(\delta) \setminus \text{RD}(\delta''))} \bar{b} \xrightarrow{a}$ .

Suppose  $a \in (\text{RD}(\delta'') \setminus \text{RD}(\delta))$ , then  $\text{WT}(\delta'') = \text{WT}(\delta) \uplus \text{RD}(\delta'') \setminus \text{RD}(\delta)$  implies that  $a \in \text{WT}(\delta'')$ , that is  $Q \xrightarrow{a}$ .

In both cases we have a contradiction because we have assumed that  $(Q \mid \prod_{b \in (\text{RD}(\delta) \setminus \text{RD}(\delta''))} \bar{b}) \not\xrightarrow{a}$ .  $\square$

We can now prove the main results of Section 4.

**Theorem A1 (Theorem 1)** *Weak asynchronous bisimulation  $\approx_a$  is a congruence.*

*Proof.* The result follows by Propositions A4-A10.  $\square$

**Theorem A2 (Theorem 2)** *Weak atomic bisimulation  $\preceq$  is a congruence.*

*Proof.* The result follows by Propositions A8 and A9.  $\square$

## B Proofs of laws in Table 4

Laws in Table 4 are proved, as usual, by showing appropriate bisimulation relations. In the following cases  $\mathcal{R}$  is the proposed bisimulation. In what follows  $a \notin \sigma$  means that the name  $a$  does not appear in  $\sigma$  and  $a^n \in \sigma$  means that  $\sigma$  contains  $n$  copies of  $a$ .

(COMM)  $\alpha.\alpha'.M \preceq \alpha'.\alpha.M$ : Suppose  $\alpha = \text{rd } a$  and  $\alpha' = \text{rd } b$  (the other cases are similar.)

$$\begin{aligned} \mathcal{R} = & \{ ((\text{rd } a.\text{rd } b.M)_{\sigma;\varepsilon}, (\text{rd } b.\text{rd } a.M)_{\sigma;\varepsilon}) \} \\ & \cup \{ ((\text{rd } b.M)_{\sigma;\text{rd } a}, (\text{rd } a.M)_{\sigma;\text{rd } b}), ((M'')_{\sigma;\text{rd } a.\text{rd } b.\delta}, (M'')_{\sigma;\text{rd } b.\text{rd } a.\delta}) \\ & \quad | a^n, b^m \in \sigma, n, m > 0, (M)_{\sigma \setminus \{a,b\};\varepsilon} \Rightarrow (M'')_{\sigma \setminus \{a,b\};\delta} \} \\ & \cup \{ ((\text{retry})_{\sigma;\varepsilon}, (\text{retry})_{\sigma;\varepsilon}) | a, b \notin \sigma \} \\ & \cup \{ ((\text{retry})_{\sigma;\varepsilon}, (\text{rd } a.M)_{\sigma;\text{rd } b}), ((\text{retry})_{\sigma;\varepsilon}, (\text{retry})_{\sigma;\text{rd } b}), \\ & \quad | a \notin \sigma, b^m \in \sigma, m > 0 \} \\ & \cup \{ ((\text{rd } b.M)_{\sigma;\text{rd } a}, (\text{retry})_{\sigma;\varepsilon}), ((\text{retry})_{\sigma;\text{rd } a}, (\text{retry})_{\sigma;\varepsilon}) \\ & \quad | a^n \in \sigma, b \notin \sigma, n > 0 \} . \end{aligned}$$



**(DIST)**  $\alpha.(M \text{ orElse } N) \preceq (\alpha.M) \text{ orElse } (\alpha.N)$ : Suppose  $M' = \text{rd } a.(M \text{ orElse } N)$  and  $N' = (\text{rd } a.M) \text{ orElse } (\text{rd } a.N)$ .

$$\begin{aligned}
\mathcal{R} = & \{((M')_{\sigma;\varepsilon}, (N')_{\sigma;\varepsilon}), ((M')_{\sigma;\varepsilon}, (\text{rd } a.M)_{\sigma;\varepsilon} \text{ orElse } (\text{rd } a.N)_{\sigma;\varepsilon})\} \\
& \cup \{((\text{retry})_{\sigma;\varepsilon}, (\text{retry})_{\sigma;\varepsilon} \text{ orElse } (\text{rd } a.N)_{\sigma;\varepsilon}), \\
& \quad ((\text{retry})_{\sigma;\varepsilon}, (\text{rd } a.M)_{\sigma;\varepsilon} \text{ orElse } (\text{retry})_{\sigma;\varepsilon}), \\
& \quad ((\text{retry})_{\sigma;\varepsilon}, (\text{retry})_{\sigma;\varepsilon} \text{ orElse } (\text{retry})_{\sigma;\varepsilon}), ((\text{retry})_{\sigma;\varepsilon}, (\text{retry})_{\sigma;\varepsilon}) \\
& \quad | a \notin \sigma\} \\
& \cup \{((M \text{ orElse } N)_{\sigma;\text{rd } a}, (M)_{\sigma;\text{rd } a} \text{ orElse } (N)_{\sigma;\text{rd } a}), \\
& \quad ((M')_{\sigma;\varepsilon}, (M)_{\sigma;\text{rd } a} \text{ orElse } (\text{rd } a.N)_{\sigma;\varepsilon}), ((M')_{\sigma;\varepsilon}, (\text{rd } a.M)_{\sigma;\varepsilon} \text{ orElse } (N)_{\sigma;\text{rd } a}) \\
& \quad | a^n \in \sigma, n > 0\} \\
& \cup \{(A \text{ orElse } (N)_{\sigma;\text{rd } a}, A \text{ orElse } (\text{rd } a.N)_{\sigma;\varepsilon}) | (M)_{\sigma;\text{rd } a} \Rightarrow A, a^n \in \sigma, n > 0\} \\
& \cup \{((M)_{\sigma;\text{rd } a} \text{ orElse } B, (\text{rd } a.M)_{\sigma;\varepsilon} \text{ orElse } B) | (N)_{\sigma;\text{rd } a} \Rightarrow B, a^n \in \sigma, n > 0\} \\
& \cup \{(A \text{ orElse } B, A \text{ orElse } B) | (M)_{\sigma;\text{rd } a} \Rightarrow A, (N)_{\sigma;\text{rd } a} \Rightarrow B, a^n \in \sigma, n > 0\} \\
& \cup \{(C, C) | (M)_{\sigma;\text{rd } a} \Rightarrow (\text{retry})_{\sigma;\delta}, (N)_{\sigma;\text{rd } a} \Rightarrow C, a^n \in \sigma, n > 0\}.
\end{aligned}$$

**(ASS)**  $M_1 \text{ orElse } (M_2 \text{ orElse } M_3) \preceq (M_1 \text{ orElse } M_2) \text{ orElse } M_3$ :

$$\begin{aligned}
\mathcal{R} = & \{((M_1 \text{ orElse } (M_2 \text{ orElse } M_3))_{\sigma;\varepsilon}, ((M_1 \text{ orElse } M_2) \text{ orElse } M_3)_{\sigma;\varepsilon}), \\
& \quad ((M_1)_{\sigma;\varepsilon} \text{ orElse } (M_2 \text{ orElse } M_3)_{\sigma;\varepsilon}, (M_1 \text{ orElse } M_2)_{\sigma;\varepsilon} \text{ orElse } (M_3)_{\sigma;\varepsilon})\} \\
& \cup \{(A \text{ orElse } (B \text{ orElse } C), (A \text{ orElse } B) \text{ orElse } C), \\
& \quad (A \text{ orElse } (M_2 \text{ orElse } M_3)_{\sigma;\varepsilon}, (A \text{ orElse } (M_2)_{\sigma;\varepsilon}) \text{ orElse } (M_3)_{\sigma;\varepsilon}), \\
& \quad ((M_1)_{\sigma;\varepsilon} \text{ orElse } ((M_2)_{\sigma;\varepsilon} \text{ orElse } C), (M_1 \text{ orElse } M_2)_{\sigma;\varepsilon} \text{ orElse } C), \\
& \quad ((M_1)_{\sigma;\varepsilon} \text{ orElse } (B \text{ orElse } (M_3)_{\sigma;\varepsilon}), ((M_1)_{\sigma;\varepsilon} \text{ orElse } B) \text{ orElse } (M_3)_{\sigma;\varepsilon}) \\
& \quad | (M_1)_{\sigma;\varepsilon} \Rightarrow A, (M_2)_{\sigma;\varepsilon} \Rightarrow B, (M_3)_{\sigma;\varepsilon} \Rightarrow C\} \\
& \cup \{((M_2 \text{ orElse } M_3)_{\sigma';\varepsilon}, (M_2)_{\sigma';\varepsilon} \text{ orElse } (M_3)_{\sigma';\varepsilon}), ((D \text{ orElse } E), D \text{ orElse } E) \\
& \quad | (M_1)_{\sigma';\varepsilon} \Rightarrow (\text{retry})_{\sigma';\delta}, (M_2)_{\sigma';\varepsilon} \Rightarrow D, (M_3)_{\sigma';\varepsilon} \Rightarrow E\} \\
& \cup \{(F, F) | (M_1)_{\sigma'';\varepsilon} \Rightarrow (\text{retry})_{\sigma';\delta}, (M_2)_{\sigma'';\varepsilon} \Rightarrow (\text{retry})_{\sigma'';\delta'}, (M_3)_{\sigma'';\varepsilon} \Rightarrow F\}.
\end{aligned}$$

**(ABSRT1)**  $\alpha.\text{retry} \preceq \text{retry}$ : suppose  $\alpha = \text{rd } a$ :

$$\begin{aligned}
\mathcal{R} = & \{((\text{rd } a.\text{retry})_{\sigma;\varepsilon}, (\text{retry})_{\sigma;\varepsilon})\} \\
& \cup \{((\text{retry})_{\sigma;\text{rd } a}, (\text{retry})_{\sigma;\varepsilon}) | a^n \in \sigma, n > 0\} \\
& \cup \{((\text{retry})_{\sigma;\varepsilon}, (\text{retry})_{\sigma;\varepsilon}) | a \notin \sigma\}.
\end{aligned}$$

**(ABSRt2)**  $\text{retry orElse } M \simeq M \simeq M \text{ orElse retry}$ :

$$\begin{aligned}\mathcal{R}_1 &= \{((\text{retry orElse } M)_{\sigma;\varepsilon}, (M)_{\sigma;\varepsilon})\} \\ &\cup \{((\text{retry})_{\sigma;\varepsilon} \text{ orElse } A, A), (A, A) \mid (M)_{\sigma;\varepsilon} \Rightarrow A\} \\ \mathcal{R}_2 &= \{((M \text{ orElse retry})_{\sigma;\varepsilon}, (M)_{\sigma;\varepsilon})\} \\ &\cup \{(A \text{ orElse } (\text{retry})_{\sigma;\varepsilon}, A) \mid (M)_{\sigma;\varepsilon} \Rightarrow A\} \\ &\cup \{((\text{end})_{\sigma;\delta}, (\text{end})_{\sigma;\delta}) \mid (M)_{\sigma;\varepsilon} \Rightarrow (\text{end})_{\sigma;\delta}\} \\ &\cup \{((\text{retry})_{\sigma;\varepsilon}, (\text{retry})_{\sigma;\delta}) \mid (M)_{\sigma;\varepsilon} \Rightarrow (\text{retry})_{\sigma;\delta}\}.\end{aligned}$$

**(ABSEnd)**  $\text{end orElse } M \simeq \text{end}$ :

$$\begin{aligned}\mathcal{R} &= \{((\text{end orElse } M)_{\sigma;\varepsilon}, (\text{end})_{\sigma;\varepsilon}), ((\text{end})_{\sigma;\varepsilon}, (\text{end})_{\sigma;\varepsilon})\} \\ &\cup \{((\text{end})_{\sigma;\varepsilon} \text{ orElse } A, (\text{end})_{\sigma;\varepsilon}) \mid (M)_{\sigma;\varepsilon} \Rightarrow A\}.\end{aligned}$$

**(ASY)**  $a.\bar{a} \approx_a \mathbf{0}$ :

$$\mathcal{R} = \{(a.\bar{a}, \mathbf{0}), (\bar{a}, \bar{a}), (\mathbf{0}, \mathbf{0})\}.$$

**(A-ASY)**  $\text{atom}(\text{rda.wt a.end}) \approx_a \mathbf{0}$ :

$$\begin{aligned}\mathcal{R} &= \{(\text{atom}(\text{rda.wt a.end}), \mathbf{0}), (\llbracket \text{rda.wt a.end} \rrbracket_{\sigma;\varepsilon}, \mathbf{0})\} \\ &\cup \{(\llbracket \text{wt a.end} \rrbracket_{\sigma;\text{rda}}, \mathbf{0}), (\llbracket \text{end} \rrbracket_{\sigma;\text{rda.wt a}}, \mathbf{0}) \mid a^n \in \sigma, n > 0\} \\ &\cup \{(\llbracket \text{retry} \rrbracket_{\sigma;\varepsilon}, \mathbf{0}), (\bar{a}, \bar{a}), (\mathbf{0}, \mathbf{0}) \mid a \notin \sigma\}.\end{aligned}$$

**(A-1)**  $\text{atom}(\text{rda.end}) \approx_a a$ :

$$\begin{aligned}\mathcal{R} &= \{(\text{atom}(\text{rda.end}), a), (\llbracket \text{rda.end} \rrbracket_{\sigma;\varepsilon}, a)\} \\ &\cup \{(\llbracket \text{end} \rrbracket_{\sigma;\text{rda}}, a), (\mathbf{0}, \mathbf{0}) \mid a^n \in \sigma, n > 0\} \\ &\cup \{(\llbracket \text{retry} \rrbracket_{\sigma;\varepsilon}, a) \mid a \notin \sigma\}.\end{aligned}$$

## C Proof of Proposition 2

In this section we show that laws in Table 4 can be used for eliminating redundant branches from an atomic expression and obtaining an equivalent expression in normal form (see proof of Proposition 2.) Some preliminary results are needed.

The next proposition states that if  $K'$ 's reads include  $K$ 's then  $K'$  is bigger than  $K$  in our weak atomic preorder.

**Proposition C1** *Suppose  $K = A_1 \cdots A_n$  and  $K' = B_1 \cdots B_m$ , with  $A_i, B_j ::= \text{rda} \mid \text{wt } a$ . If  $\text{RD}(K) \subseteq \text{RD}(K')$  then  $K \sqsupseteq K'$ .*

*Proof.* It is enough to observe that if  $(K')_{\sigma;\varepsilon} \Rightarrow (\text{end})_{\sigma;\delta}$  then  $\text{RD}(K') \subseteq \sigma$  (rules (ARDOK) and (ARDF)); thus  $\text{RD}(K) \subseteq \sigma$ , and by (ARDOK) we get  $(K)_{\sigma;\varepsilon} \Rightarrow (\text{end})_{\sigma;\delta}$ .  $\square$

As a consequence of the previous proposition, we obtain that, in an `orElse` expression, a redundant branch, that is a branch which includes the reads of at least one of its preceding branches, can be eliminated.

**Proposition C2** *Consider the expressions  $K_1, \dots, K_n$  where, for  $i = 1, \dots, n$ ,  $K_i$  is of the form  $A_{i_1} \dots A_{i_{n_i}}$  with  $A_{i_j} ::= \text{rd } a \mid \text{wt } a$ . If  $\text{RD}(K_j) \subseteq \text{RD}(K_n)$ , for a  $j$  such that  $0 < j < n$ , then*

$$K_1 \text{ orElse } \dots \text{ orElse } K_{n-1} \text{ orElse } K_n \simeq K_1 \text{ orElse } \dots \text{ orElse } K_{n-1}.$$

*Proof.* The proof proceeds by using Proposition C1, the fact that  $M \sqcup M' \simeq M$  if and only if  $M \sqsupseteq M'$  (see pag. 13) and `orElse`'s rules in Table 3.  $\square$

As previously said, the proof of the following theorem show how to apply rules in Table 4 for rearranging an atomic expression into an equivalent one in normal form.

**Proposition C3 (Proposition 2)** *For every expression  $M$  there is an expression  $M'$  in normal form such that  $M \simeq M'$ .*

*Proof.* The proof proceeds by induction on the structure of  $M$ :

$M = \text{end};$   $M' = M = \text{end};$

$M = \text{retry};$   $M' = M = \text{retry};$

$M = \alpha.N;$  by induction hypothesis, there is an  $N'$  in normal form such that  $N \simeq N'$ . By Proposition A8,  $\alpha.N \simeq \alpha.N'$ , thus by choosing  $M' = \alpha.N'$  we obtain  $M \simeq M'$ ;

$M = N \text{ orElse } N';$  by induction hypothesis, there are  $N_0$  and  $N'_0$ , in normal form, such that  $N \simeq N_0$  and  $N' \simeq N'_0$ . By Proposition A9,  $M = N \text{ orElse } N' \simeq N_0 \text{ orElse } N'_0$ . We choose  $M'$  by considering the following cases:

- if  $N_0 = \text{retry}$  we choose  $M' = N'_0$ , because, by (ABSRT),  $\text{retry orElse } N'_0 \simeq N'_0$ ;
- if  $N_0 = N_{0_1} \text{ orElse } \dots \text{ orElse } N_{0_n}$  and  $N'_0 = N'_{0_1} \text{ orElse } \dots \text{ orElse } N'_{0_m}$ , consider  $P = \{j \mid k \in \{1, \dots, n\} : \text{RD}(N_{0_k}) \subseteq \text{RD}(N'_{0_j})\}$ . If  $P = \emptyset$  this means that  $M' = N_0 \text{ orElse } N'_0$  is in normal form.

Otherwise, suppose  $P = \{j_1, \dots, j_l\}$  with  $j_i < j_w$  for  $i < w$ ; by applying Proposition C2 and A9 and (ASS) at every step, we have

$$\begin{aligned}
& N_0 \text{ orElse } N'_0 \\
& \simeq \quad (\text{by removing } N'_{0_{j_1}}) \\
& N_0 \text{ orElse } N'_{0_1} \text{ orElse } \cdots \text{ orElse } N'_{0_{j_1-1}} \text{ orElse } N'_{0_{j_1+1}} \text{ orElse } \cdots \text{ orElse } N'_{0_m} \\
& \simeq \quad (\text{by removing } N'_{0_{j_2}}) \\
& N_0 \text{ orElse } N'_{0_1} \text{ orElse } \cdots \text{ orElse } N'_{0_{j_1-1}} \text{ orElse } N'_{0_{j_1+1}} \text{ orElse } \cdots \\
& \quad \text{ orElse } N'_{0_{j_2-1}} \text{ orElse } N'_{0_{j_2+1}} \text{ orElse } \cdots \text{ orElse } N'_{0_m} \\
& \simeq \quad (\text{by removing } N'_{0_{j_3}}) \\
& \vdots \\
& \simeq \quad (\text{by removing } N'_{0_{j_l}}) \\
& N_0 \text{ orElse } N'_{0_1} \text{ orElse } \cdots \text{ orElse } N'_{0_{j_1-1}} \text{ orElse } N'_{0_{j_1+1}} \text{ orElse } \cdots \\
& \quad \text{ orElse } N'_{0_{j_2-1}} \text{ orElse } N'_{0_{j_2+1}} \text{ orElse } \cdots \text{ orElse } N'_{0_{j_l-1}} \\
& \quad \text{ orElse } N'_{0_{j_l+1}} \text{ orElse } \cdots \text{ orElse } N'_{0_m} \\
& = M' \text{ (that is in normal form.)}
\end{aligned}$$

In every case,  $M' \simeq N_0 \text{ orElse } N'_0$ , thus  $M \simeq M'$ .  $\square$

## D Proofs of Section 5

**Lemma D1 (Lemma 1)** Assume that  $s' \preceq s$  and  $P \xRightarrow{\bar{s}} P'$ , then there is a process  $P''$  such that  $P \xRightarrow{\bar{s}'} P''$ .

*Proof.*  $s' \preceq s$  means  $s' \preceq_0^n s$ , for some  $n \geq 0$ . The proof proceeds by induction on  $n$ . For  $n = 0$  we have  $s = s'$ . Suppose  $n > 0$  and  $s' \preceq_0^{n-1} s'' \preceq_0 s$ . The result follows by induction hypothesis if we show that  $P \xRightarrow{\bar{s}'} P''$ . We proceed by distinguishing the possible cases for  $s'' \preceq_0 s$  according to laws (TO1)-(TO4).

- (TO1)  $s'' = rr'$  and  $s = r\{a\}r'$ , thus  $\bar{s}'' = \bar{r}\bar{r}'$  and  $\bar{s} = \bar{r}\bar{a}\bar{r}'$ .  $P \xRightarrow{\bar{s}} P'$  implies  $P \xRightarrow{\bar{r}} P_1 \xRightarrow{\bar{a}} P_2 \xRightarrow{\bar{r}'}$ , and by Proposition A1,  $P_1 \sim P_2 | \bar{a}$ , that is  $P \xRightarrow{\bar{r}} P_2 | \bar{a} \xRightarrow{\bar{r}'}$ , hence  $P \xRightarrow{\bar{s}'} P''$ ;
- (TO2)  $s'' = rl\{a\}r'_r$  and  $s = r\{a\}lr'$ , thus  $\bar{s}'' = \bar{r}\bar{l}\bar{a}\bar{r}'$  and  $\bar{s} = \bar{r}\bar{a}\bar{l}\bar{r}'$ .  $P \xRightarrow{\bar{s}} P'$  implies  $P \xRightarrow{\bar{r}} P_1 \xRightarrow{\bar{a}} P_2 \xRightarrow{\bar{r}'} P_3 \xRightarrow{\bar{l}}$ , and by Proposition A1,  $P_1 \sim P_2 | \bar{a}$ , that is  $P \xRightarrow{\bar{r}} P_2 | \bar{a} \xRightarrow{\bar{r}'} P_3 | \bar{a} \xRightarrow{\bar{l}}$ , hence  $P \xRightarrow{\bar{s}'} P''$ ;
- (TO3)  $s'' = rr'$  and  $s = r\{a\}\bar{a}r'$ , thus  $\bar{s}'' = \bar{r}\bar{r}'$  and  $\bar{s} = \bar{r}\bar{a}\{a\}\bar{r}'$ .  $P \xRightarrow{\bar{s}} P'$  implies  $P \xRightarrow{\bar{r}} P_1 \xRightarrow{\bar{a}} P_2 \xRightarrow{\bar{a}} P_3 \xRightarrow{\bar{r}'}$ , hence, by Proposition A1,  $P_1 \sim P_2 | \bar{a}$ , that is  $P_2$  can synchronize with  $\bar{a}$  and  $P \xRightarrow{\bar{r}} P_2 | \bar{a} \xRightarrow{\bar{r}'} P_3 \xRightarrow{\bar{r}'}$ , that is  $P \xRightarrow{\bar{s}'} P''$ ;
- (TO4)  $s'' = \{a_1\} \cdots \{a_n\}$  and  $s = \{a_1, \dots, a_n\}$ , or viceversa; in this case  $\bar{s} = \bar{s}''$  by definition of  $\bar{\cdot}$ .  $\square$

**Lemma D2 (Lemma 2)** Consider two traces  $s$  and  $r$ . If there is a process  $Q$  such that  $O(s) \xRightarrow{\bar{r}} \xRightarrow{\bar{w}} Q$  then  $r \preceq s$ .

*Proof.* The proof proceeds by induction on  $s$ .

$s = \bar{a}s'$ :  $O(s) = a.O(s')$  and  $O(s) \xrightarrow{\bar{w}}$  implies  $\bar{r} = a\bar{r}'$  such that  $O(s) \xrightarrow{\{a\}} O(s') \xrightarrow{\bar{r}'}$ . By induction hypothesis,  $r' \preceq s'$ , hence by prefixing,  $r = \bar{a}r' \preceq \bar{a}s' = s$ ;

$s = \{a_1, \dots, a_n\}s'$ :  $O(s) = (\prod_{a \in \{a_1, \dots, a_n\}} \bar{a}) \mid O(s')$ . We have  $O(s) \xrightarrow{\bar{w}}$ , we can distinguish the following cases depending on  $\bar{r}$ :

$\bar{a}_i \notin \bar{r}$ : by induction hypothesis,  $O(s') \xrightarrow{\bar{w}}$  implies  $r \preceq s'$  and by (TO1),  $r \preceq s' \preceq \{a_1\} \dots \{a_n\}s' \preceq_0 \{a_1, \dots, a_n\}s' = s$ ;

$\bar{a}_{i_1}, \dots, \bar{a}_{i_k} \in \bar{r}$  for  $\{a_{i_1}, \dots, a_{i_k}\} \subseteq \{a_1, \dots, a_n\}$ : in this case  $\bar{r} = \bar{r}_1\bar{a}_{i_1} \dots \bar{r}_k\bar{a}_{i_k}\bar{r}_{k+1}$  and  $O(s') \xrightarrow{\bar{r}_1 \dots \bar{r}_{k+1}w}$ . By induction hypothesis,  $r_1 \dots r_{k+1} \preceq s'$ :

$$\begin{aligned}
r &= r_1\{a_{i_1}\} \dots r_k\{a_{i_k}\}r_{k+1} \\
&\preceq \{a_{i_1}\} \dots \{a_{i_k}\}r_1 \dots r_{k+1} \text{ (by (TO2))} \\
&\preceq \{a_{i_1}\} \dots \{a_{i_k}\}s' \text{ (by induction and prefixing)} \\
&\preceq \{a_1\} \dots \{a_n\}s' \text{ (by (TO1) and (TO2))} \\
&\preceq_0 \{a_1 \dots a_n\}s' \text{ (by (TO4))} \\
&= s;
\end{aligned}$$

$\bar{r} = \bar{r}_1 \dots \bar{r}_k$  and  $O(s') \xrightarrow{\bar{r}_1\{a_{i_1}\} \dots \bar{r}_k\{a_{i_k}\}r_{k+1}}$  for  $\{a_{i_1}, \dots, a_{i_k}\} \subseteq \{a_1, \dots, a_n\}$ : by induction hypothesis,  $r_1\bar{a}_{i_1} \dots r_k\bar{a}_{i_k}r_{k+1} \preceq s'$  and:

$$\begin{aligned}
r &= r_1 \dots r_k \\
&\preceq r_1\{a_{i_1}\}\bar{a}_{i_1} \dots r_k\{a_{i_k}\}\bar{a}_{i_k}r_{k+1} \text{ (by (TO3))} \\
&\preceq \{a_{i_1}\} \dots \{a_{i_k}\}r_1\bar{a}_{i_1} \dots r_k\bar{a}_{i_k}r_{k+1} \text{ (by (TO2))} \\
&\preceq \{a_{i_1}\} \dots \{a_{i_k}\}s' \text{ (by induction)} \\
&\preceq \{a_1\} \dots \{a_n\}s_0 \text{ (by (TO1) and (TO2))} \\
&\preceq_0 \{a_1 \dots a_n\}s' \text{ (by (TO4))} \\
&= s.
\end{aligned}$$

□

The proof of the full-abstraction theorem is standard (see e.g. [7]).

**Theorem D1 (Theorem 3)** For all processes  $P$  and  $Q$ ,  $P \sqsubseteq_{\text{may}} Q$  if and only if  $P \ll_{\text{may}} Q$ .

*Proof.*  $\Rightarrow$ : Suppose  $P \ll_{\text{may}} Q$  and  $P \text{ may } O$  for any observer  $O$  we have to show that  $Q \text{ may } O$ .  $P \text{ may } O$  means that  $P \mid O \xrightarrow{\bar{w}}$ , that is there exists a trace  $s$  such that  $P \xrightarrow{s}$  and  $O \xrightarrow{\bar{w}}$ .  $P \ll_{\text{may}} Q$  implies that there exists  $s' \preceq s$  such that  $Q \xrightarrow{s'}$ .  $s' \preceq s$  implies  $s'w \preceq sw$ . By Lemma D1 and  $O \xrightarrow{\bar{w}}$  we get that  $O \xrightarrow{s'w}$ . Hence, from  $Q \xrightarrow{s'}$  we obtain  $Q \mid O \xrightarrow{\bar{w}}$ , that is  $Q \text{ may } O$  ( $P \sqsubseteq_{\text{may}} Q$ ).

$\Leftarrow$ : Suppose  $P \sqsubseteq_{\text{may}} Q$  and  $P \xrightarrow{s}$ , we have to show that there exists  $s' \preceq s$  such that  $Q \xrightarrow{s'}$ . From  $P \xrightarrow{s}$  and  $O(s) \xrightarrow{\bar{w}}$  we have  $P \mid O(s) \xrightarrow{\bar{w}}$ , that is  $P \text{ may } O(s)$ . Hence  $Q \text{ may } O(s)$ , that is  $Q \mid O(s) \xrightarrow{\bar{w}}$ . Thus, there exists  $s'$  such that  $Q \xrightarrow{s'}$  and  $O(s) \xrightarrow{s'w}$ , and, by Lemma D2 and  $O(s) \xrightarrow{s'w}$  we have  $s' \preceq s$ , that is  $P \ll_{\text{may}} Q$ . □

**Lemma D3 (Lemma 3)** Assume  $M = \sqcup_{i \in 1..n} K_i$  is an expression in normal form. For every index  $i$  in  $\{1, \dots, n\}$  we have  $\text{atom}(M); \sigma_i \rightarrow^* \{(\text{end})_{\sigma_i; \delta}\}_M; \sigma_i$  where  $\sigma_i = \text{RD}(K_i) = \text{RD}(\delta)$  and  $\text{WT}(\delta) = \text{WT}(K_i)$ .

*Proof.* By definition of normal form.  $\square$

**Corollary D1** Assume  $M = \sqcup_{i \in 1..n} K_i$  is an expression in normal form. The possible behavior of  $\text{atom}(M)$  can be described as  $\text{atom}(M) \xrightarrow{\sigma_i} \prod_{b \in \text{WT}(r_i)} b$  for every  $i \in 1..n$  where  $\sigma_i$  is the multiset  $\text{RD}(K_i)$ .

*Proof.* By Lemma D3, rule (ATOK) and definition of  $\xrightarrow{\mu}$ .  $\square$

We can prove now the main result of Section 5, that is that may-testing semantics is not able to distinguish the behavior of an atomic expression from the behavior of the corresponding CCS process.

**Theorem D2 (Theorem 3)** For every expression  $M$  in normal form we have  $\text{atom}(M) \simeq_{\text{may}} \llbracket M \rrbracket$ .

*Proof.* The proof proceeds by using the alternative preorder instead of the may preorder; in what follows it is shown that:

1.  $\text{atom}(M) \ll_{\text{may}} \llbracket M \rrbracket$ ;
2.  $\llbracket M \rrbracket \ll_{\text{may}} \text{atom}(M)$ .

Remember that  $M$  is in normal-form, thus  $M = \text{OrElse}_{i=1, \dots, n} K_i$  and  $\llbracket M \rrbracket = \sum_{i=1, \dots, n} \llbracket K_i \rrbracket$ . The two points are shown in what follows.

1. For proving that  $\text{atom}(M) \ll_{\text{may}} \llbracket M \rrbracket$ , we have to show that  $\forall s$  such that  $\text{atom}(M) \xrightarrow{s}$  there exists  $s' \preceq s$  such that  $\llbracket M \rrbracket \xrightarrow{s'}$ . We distinguish the following cases for  $s$ :  
 $s = \varepsilon$ : in this case we can choose  $s' = \varepsilon$ ;  
 $s = \delta \bar{a}_{i_1} \dots \bar{a}_{i_l}$  with  $l \geq 0$ : by Corollary D1, there is a  $j \in \{1, \dots, n\}$  such that  $\delta = \text{RD}(K_j)$ ,

$$\text{atom}(M) \xrightarrow{\text{RD}(K_j)} \bar{a}_1 \mid \dots \mid \bar{a}_m \xrightarrow{\bar{a}_{i_1} \dots \bar{a}_{i_l}}$$

with  $\{a_{i_1}, \dots, a_{i_l}\} \subseteq \{a_1, \dots, a_m\} = \text{WT}(K_j)$ .

Suppose  $\text{RD}(K_j) = \{b_1, \dots, b_k\}$ . By definition,  $\llbracket K_j \rrbracket = b_1 \dots b_k.(\bar{a}_1 \mid \dots \mid \bar{a}_m)$  with  $\{a_1, \dots, a_m\} = \text{WT}(K_j)$ . That is, if we choose the  $j$ -th summands of  $\llbracket M \rrbracket$ , we have  $\llbracket M \rrbracket \xrightarrow{s'}$  with  $s' = \{b_1\} \dots \{b_k\} \bar{a}_{i_1} \dots \bar{a}_{i_l}$ , and by (TO4)  $s'_0 \succeq_0 s$ ;

2. For proving that  $\llbracket M \rrbracket \ll_{\text{may}} \text{atom}(M)$ , we have to show that  $\forall s$  such that  $\llbracket M \rrbracket \xrightarrow{s}$  there exists  $s' \preceq s$  such that  $\text{atom}(M) \xrightarrow{s'}$ . We distinguish the following cases for  $s$ :  
 $s = \{b_1\} \dots \{b_k\}$ :  $s$  contains only input actions, thus we can choose  $s' = \varepsilon \preceq s$  and  $\text{atom}(M) \xrightarrow{s'}$ ;  
 $s = \{b_1\} \dots \{b_k\} \bar{a}_1 \dots \bar{a}_m$  with  $m > 0$ : in this case there is a  $j \in \{1, \dots, n\}$  such that  $\llbracket K_j \rrbracket \xrightarrow{s}$ ,  $\{b_1, \dots, b_k\} = \text{RD}(K_j)$  and  $\{a_1, \dots, a_m\} \subseteq \text{WT}(K_j)$  (by definition of  $\llbracket \cdot \rrbracket$ ). Suppose  $\sigma = \text{RD}(K_j)$ , by Lemma D3,  $\text{atom}(M); \sigma \Rightarrow \{(\text{end})_{\sigma; \delta}\}_M$  with  $\text{RD}(\delta) = \text{RD}(K_j)$  and  $\text{WT}(\delta) = \text{WT}(K_j)$ . This means that  $\text{atom}(M) \xrightarrow{\text{RD}(K_j)} \prod_{a \in \text{WT}(K_j)} \bar{a}$ , that is (by (TO4)) there is an  $s' = \text{RD}(\delta) \bar{a}_1 \dots \bar{a}_m \succeq_0 \{b_1\} \dots \{b_k\} \bar{a}_1 \dots \bar{a}_m = s$  such that  $\text{atom}(M) \xrightarrow{s'}$ .  $\square$